

# The Machinery of Interaction

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## ABSTRACT

This paper revisits the Interaction Abstract Machine (IAM), a machine based on Girard’s Geometry of Interaction, introduced by Mackie and Danos & Regnier. It is an unusual machine, not relying on environments, presented on linear logic proof nets, and whose soundness proof is convoluted and passes through various other formalisms. Here we provide a new direct proof of its correctness, based on a variant of Sands’s improvements, a natural notion of bisimulation. Moreover, our proof is carried out on a new presentation of the IAM, defined as a machine acting directly on  $\lambda$ -terms, rather than on linear logic proof nets.

## CCS CONCEPTS

• Theory of computation  $\rightarrow$  Lambda calculus; Abstract machines.

## KEYWORDS

geometry of interaction, lambda-calculus, abstract machines

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## 1 INTRODUCTION

The advantage, and at the same time the drawback, of the  $\lambda$ -calculus is its distance from low-level implementation details. It comes with just one rule,  $\beta$ -reduction, and with no indication about how to implement it on low-level machines. It is an advantage when *reasoning* about programs expressed as  $\lambda$ -terms. It is a drawback, instead, when one wants to *implement* the  $\lambda$ -calculus, or to do complexity analyses, because  $\beta$ -steps are far from being atomic operations. In particular, terms can grow exponentially with the number of  $\beta$ -steps, a degeneracy known as *size explosion*, which is why  $\beta$ -reduction cannot be reasonably implemented, at least if one sticks to an explicit representation of  $\lambda$ -terms.

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*Environment Machines.* Implementations solve this issue by evaluating the  $\lambda$ -calculus up to *sharing of sub-terms*, where sharing is realized through a data structure called *environment*, collecting the sharing annotations generated by the machine, one for each encountered  $\beta$ -redex. For common *weak* evaluation strategies (i.e. that do not inspect in scope of  $\lambda$ -abstractions) such as call-by-name/value/need, the number of  $\beta$ -steps is a reasonable time cost model [16, 22, 50]. Environment machines—whose most famous examples are Landin’s SECD [39], Felleisen and Friedman’s CEK [29], or Krivine’s KAM [38]—can be optimized to run within a linear overhead with respect to the number of  $\beta$ -steps [10, 14]. Said differently, they *respect* the time cost model (see [3] for an overview). For space, the situation is different. Only recently the problem has been tackled [31] and some preliminary and limited results have appeared. Then, environment machines store information for every  $\beta$ -step, therefore using space linear in time, which is *the worst possible use of space*<sup>1</sup>.

*Beyond Environments.* Frameworks based on the  $\lambda$ -calculus are invariably implemented using environments. Nonetheless, the lack of a fixed execution schema for the  $\lambda$ -calculus leaves open, in theory, the possibility of alternative implementation schemes. The theory of linear logic indeed provides a completely different style of abstract machines, rooted in Girard’s Geometry of Interaction [33] (shortened to GoI in the following). These GoI machines were pioneered by Mackie and Danos & Regnier in the nineties [27, 41]. The basic idea is that the machine *does not use environments*, and instead uses a data structure called *token*, saving information about the history of the computation. The key point is that the token does not store information about every single  $\beta$ -redex, thus disentangling space-consumption from time-consumption. In other words, GoI machines are good candidates for space-efficient implementation schemes, as first shown by Schöpp and coauthors [23, 51]. The price to pay is that the machine wastes a lot of time to retrieve  $\beta$ -redexes, so that time is sacrificed for space. The same, however, happens with space-sensitive Turing machines.

*The Interaction Abstract Machine.* The original GoI machine is the *Interaction Abstract Machine* (IAM). It was developed at the same time by Mackie and Danos & Regnier, and its first appearance is in a paper by Mackie in 1995 [41], dealing with implementations. Danos and Regnier study it in two papers, one in 1996 together with Herbelin [25], where it is dealt with quickly, and its implementation theorem (or correctness<sup>2</sup>) is proved via game semantics, and one by themselves [27], published only in 1999 but reporting work dating back to a few years before, dedicated to the IAM and to an

<sup>1</sup>On sequential models space cannot exceed time, as one needs a unit of time to use a unit of space.

<sup>2</sup>The result that an abstract machine implements a strategy is sometimes called *correctness* of the machine. We prefer to avoid such a terminology, because it suggests the existence of a dual *completeness* result, that is never given because already contained in the statement of correctness. We then simply talk of an *implementation theorem*.

optimization based on a fine analysis of IAM runs. These papers differ on many details but they all formulate the IAM on linear logic proof-nets as a reversible, bideterministic automaton.

In [25], Danos, Herbelin, and Regnier prove that the IAM implements *linear head evaluation*  $\rightarrow_{lh}$  (shortened to LHE), a refinement of head evaluation, arising from the linear logic decomposition of the  $\lambda$ -calculus. Their proof of the implementation theorem for the IAM—the only one in the literature—is indirect and rooted in game semantics, as it follows from a sequence of results relating the IAM to AJM games, AJM games to HO games, HO games to another abstract machine, the PAM, and finally the PAM to LHE. Moreover, the proof is technical, the main ingredients unclear, and not as neat as for environment machines.

*New Proof of the Implementation Theorem.* The main contribution of this paper is an alternative proof of the implementation theorem for the IAM, which is independent of game semantics and other abstract machines. Our proof is direct and based on a natural notion of bisimulation, namely a variation on Sands’ *improvements* [49].

The implementation theorem of GoI machines amounts to showing that their result is an adequate and sound semantics for LHE, that is, it is invariant by LHE (soundness) and it is non-empty if and only if LHE terminates (adequacy). The key point for soundness is that—in contrast to the study of environment machines—steps of the GoI machine are not mapped to LHE steps, because the GoI computes differently. What is shown is that if  $t \rightarrow_{lh} u$  then the run of the machine on  $t$  is “akin” to the run on  $u$ , and they produce akin results—see Sect. 2 for more details. In our proof, “akin” is interpreted as *bisimilar*. An *improvement* is a bisimulation asking that the run on  $u$  is no longer than the run on  $t$ . Building on such a quantitative refinement, we prove adequacy.

The proof of our implementation theorem is arguably conceptually simpler than Danos, Herbelin, and Regnier’s. Of course, their deep connection with game semantics is an important contribution that is not present here. We believe, however, that having independent and simpler proof techniques is also valuable.

*The Lambda Interaction Abstract Machine.* The second contribution of the paper is a formulation of the IAM as a machine acting directly on  $\lambda$ -terms rather than on linear logic proof nets. Our proof might also have been carried out on proof nets, but we prefer switching to  $\lambda$ -terms for two reasons. First, manipulating terms rather than proof nets is easier and less error-prone for the technical development. Second, we aim at minimizing the background required for understanding the IAM, and so doing we remove any explicit reference to linear logic and graphical syntaxes.

The starting point of our *Lambda Interaction Abstract Machine* ( $\lambda$ -IAM) is seeing a position in the code  $t$  (what is usually the position of the token on the proof net representation of  $t$ ) as a pair  $(u, C)$  of a sub-term  $u$  and a context  $C$  such that  $C\langle u \rangle = t$ . These positions are simply a readable presentation of pointers<sup>3</sup>.

The main novelty of the new presentation is that some of the exponential transitions on proof nets are packed together in macro transitions. The shape of our transitions makes a sort of backtracking mechanism more evident. *Careful*: that the IAM rests on

backtracking is the key point of Danos and Regnier in [27], and therefore it is not a novelty in itself. What is new is that such a mechanism is already visible at the level of transitions, while on proof nets it requires sophisticated analyses of runs.

It may be argued that linear logic provides a useful conceptual framework for the GoI. While this is undeniable, we are trying to show that linear logic is however not needed, and that an alternative presentation provides other useful intuitions—the two presentations give different insights, and thus complement each other. The easy correspondence between the two is stated in Sect. 10.

*More About the  $\lambda$ -IAM.* The original papers on the IAM [25, 27, 41] differ on many points. Here we follow [25], modeling the  $\lambda$ -IAM on the call-by-name translation of the  $\lambda$ -calculus in linear logic and considering only the path/runs starting on the distinguished conclusion corresponding to the output of the net/term. This is natural for terms, and also along the lines of how AJM games interpret terms. Similarly to AJM games, then, our GoI semantics is sound also for open terms with respect to erasing steps.

An original point of our work is the identification of a new invariant of the  $\lambda$ -IAM—probably of independent interest—based on what we call *exhaustible states*. Informally, a state of the  $\lambda$ -IAM is exhaustible if its token can be emptied in a certain way, somehow mimicking the computation which leads to the state itself.

*This Paper in Perspective.* This paper is part of a long-time endeavor by the authors directed at understanding complexity measures and implementation schemas for the  $\lambda$ -calculus. We provide a new proof technique for GoI implementation theorems not relying on game semantics, together with a new presentation of the original machine by Mackie and Danos & Regnier not relying on linear logic. The aim is to set the ground for a formal, robust, and systematic study of GoI machines and their complexity, while at the same time shrinking to the minimum the required background. A further motivation behind our work is the desire to make the study of GoI machines easier to formalize in proof assistants, as proof nets are particularly cumbersome in that respect.

*Related Work on GoI.* This is certainly *not* the first paper on the GoI and the  $\lambda$ -calculus. Indeed, the literature on the topic and its applications is huge, and goes from Girard’s original papers [33], to Abramsky *et al*’s reformulation using the INT-construction [1], Danos and Regnier’s using path algebras [26], Ghica’s applications to circuit synthesis [32], together with extensions by Hoshino, Muroya, and Hasuo to languages with various kinds of effects [35], and Laurent’s extension to the additive connectives of linear logic [40]. In all these cases, the GoI interpretation, even when given on  $\lambda$ -terms, goes *through* linear logic (or symmetric monoidal categories) in an essential way. The only notable exceptions are perhaps the recent contributions by Schöpp on the relations between GoI, CPS, and defunctionalization [52, 53] in which, indeed, some deep relations are shown to exist between GoI and classic tools in the theory of  $\lambda$ -calculus. Even there, however, GoI is seen as obtained through the INT-construction [1, 36], although applied to a syntactic category of terms.

The GoI has been studied in relationship with implementations of functional languages, by Gonthier, Abadi and Levy, who studied

<sup>3</sup>For the acquainted reader, they play a role akin to the initial labels in Lévy’s labeled  $\lambda$ -calculus, itself having deep connections with the IAM [15].

Lévy’s optimal evaluation [34], and by Mackie with his GoI machine for PCF [41], Gödel System T [42], and—with Fernandez—for call-by-value [30]. The space-efficiency studied by Dal Lago and Schöpp [23] is exploited by Mazza in [44] and, together with Terui, in [45]. Dal Lago and coauthors have also introduced variants of the IAM acting on proof nets for a number of extensions of the  $\lambda$ -calculus [19–21, 24]. Curien and Herbelin study abstract machines related to game semantics and the IAM in [17, 18]. Muroya and Ghica have recently studied the GoI in combination with rewriting and abstract machines in [47]. The already cited works by Schöpp [52, 53] highlight how GoI can be seen as an optimized form of CPS transformation, followed by defunctionalization.

*Related Work on Environment Machines.* The time efficiency of environment machines has been recently closely scrutinized. Before 2014, the topic had been mostly neglected—the only two counterexamples being Belloch and Greiner in 1995 [16] and Sands, Gustavsson, and Moran in 2002 [50]. Since 2014—motivated by advances by Accattoli and Dal Lago on time cost models for the  $\lambda$ -calculus [8]—Accattoli and co-authors have explored time analyses of environment machines from different angles [5–7, 10].

*Proofs.* Omitted proofs can be found in [13].

## 2 IMPLEMENTATIONS AND IMPROVEMENTS

In this section we explain in which sense the  $\lambda$ -IAM implements head evaluation. The  $\lambda$ -IAM itself shall be defined in the next sections, here we rather provide a high-level perspective on the differences between the  $\lambda$ -IAM and environment machines. We shall also define *improvements*, the notion of bisimulation that we use to prove the implementation theorems.

*(Restricted) Head Evaluation.* The  $\lambda$ -IAM implements *head evaluation*, the simple reduction relation defined as:

$$\lambda x_1 \dots \lambda x_i. (\lambda y. t) u r_1 \dots r_j \rightarrow_h \lambda x_1 \dots \lambda x_i. t \{y \leftarrow u\} r_1 \dots r_j.$$

where  $i, j \geq 0$ . Note that  $\rightarrow_h$  reduces under abstractions, but only head abstractions, and that terms may be open. For a fixed  $i \in \mathbb{N}$ , the restriction  $\rightarrow_{h_i}$  of  $\rightarrow_h$  is obtained by forbidding  $\rightarrow_h$  to reduce under more than  $i$  head abstractions.

*Machines.* We need some basic terminology about machines. A *machine*  $M = (s, \rightarrow)$  is a transition system  $\rightarrow$  over a set of states, noted  $s$ . A potentially empty sequence of transitions is a *run*. A state is usually given by a code, that is a  $\lambda$ -term, and a set of data structures. Among the data structures of environment machines there are always *environments*, that record the previously encountered  $\beta$ -redexes. The  $\lambda$ -IAM instead does not have any environment, because it uses a different mechanism to compute. Indeed, there are (at least) three key differences between environment machines and the  $\lambda$ -IAM, that we now overview.

*Difference 1: Initial States and Depth.* In the initial state  $s_t$  of code  $t$  for an environment machine, all data structures are empty. Environment machines, additionally, are either weak (that is, *never* enter abstractions) or strong (they enter into *all* abstractions). In contrast, the  $\lambda$ -IAM is *incrementally strong*, *i.e.*, it has a finer mechanism for entering into *some* abstractions. The number of head abstractions that a run of the  $\lambda$ -IAM can cross, called the *depth* of the run, is specified by initializing the token with a natural number  $i$ . Note

the difference with environment machines: once the code  $t$  is fixed, such machines have only one initial state  $s_t$ , while the  $\lambda$ -IAM has a *family* of initial states  $\{s_{t,i}\}_{i \in \mathbb{N}}$ , one for each depth  $i$ .

Therefore, the  $\lambda$ -IAM *approximates* head reduction: a run from  $s_{t,i}$  essentially correspond to evaluation with respect to the restricted  $i$ -head evaluation  $\rightarrow_{h_i}$ , and one approximates head reduction by executing the  $\lambda$ -IAM on increasing values of  $i$ .

*Difference 2: Evaluating Without Simulating the Rewriting.* The way in which the  $\lambda$ -IAM implements  $\rightarrow_h$  is fundamentally different from the way in which environment machines implement strategies. Roughly, environment machines simulate the strategy, while the  $\lambda$ -IAM computes/approximates a semantics for it, as we now explain.

*Implementation Theorem for Environment Machines.* An environment abstract machine  $M$  implements a strategy  $\rightarrow$  if from the initial state  $s_t$  of code  $t$  it computes a representation of the normal form  $\text{nf}_{\rightarrow}(t)$ . In particular, the machine somehow maintains the representation of how the strategy  $\rightarrow$  modifies the term  $t$  they both evaluate. The implementation theorem states a weak bisimulation between the transitions  $s \rightarrow_M s'$  of the machine and the steps  $t \rightarrow u$  of the strategy. In particular, a run  $\rho_t$  of the machine on  $t$  passes through some states representing  $u$ .

*Implementation Theorem(s) for the  $\lambda$ -IAM.* The  $\lambda$ -IAM, and more generally GoI machines, do implement strategies, but in a different way. The key point is that these machines do not trace how the strategy modifies the term. If  $t \rightarrow_h u$ , a  $\lambda$ -IAM run of code  $t$  never passes through a representation of  $u$ , as *implementing*  $\rightarrow_h$  here denotes something else. There are actually *two* implementation theorems, called *soundness* and *adequacy*. Soundness is the fact that, at a fixed depth, the run of code  $t$  is bisimilar to the run of code  $u$ . Note the difference with environment machines: there, the bisimulation is between *steps* on terms and transitions on states. For the  $\lambda$ -IAM, it is between *transitions* on states (of code  $t$ ) and transitions on states (of code  $u$ ). The important consequence of soundness is that the runs from  $t$  and  $u$  produce bisimilar final states (if they terminate). The idea is that these final states are different but encode the same description of the normal form of  $t$  and  $u$ , which is the semantics  $\llbracket t \rrbracket$  induced by the  $\lambda$ -IAM.

Adequacy guarantees that the interpretation  $\llbracket t \rrbracket$  reflects some observable aspects of  $t$ . For a head evaluation, one usually observes termination, and, if it holds, the identity of the head variable. This is exactly what  $\llbracket t \rrbracket$  reflects, or it is adequate for.

To be precise, the interpretation  $\llbracket t \rrbracket$  is parametric in the depth  $i$ , thus  $\llbracket t \rrbracket_i$  is a semantics for  $\rightarrow_{h_i}$ , amounting to specifying the 4 possible outcomes of evaluating  $t$  with  $\rightarrow_{h_i}$ : divergence, termination on a head normal form whose head variable is free or bound (these are two different outcomes), or termination but not on a head normal form (that is, on a term of the form  $\lambda x_1 \dots \lambda x_i. \lambda y. t$ , indicating that one has to consider the more informative approximation  $\llbracket t \rrbracket_{i+1}$ , inspecting under  $\lambda y$ ). Soundness then is the fact that if  $t \rightarrow_h u$  then  $\llbracket t \rrbracket_i = \llbracket u \rrbracket_i$  for all  $i$ , while adequacy is the fact that there is  $i$  such that  $\llbracket t \rrbracket_i$  finds a head variable if and only if  $\rightarrow_h$  terminates.

*Difference 3: On Not Computing Results.* Environment machines implementing a strategy  $\rightarrow$  stop on final states  $s_f$  decoding to  $\rightarrow$ -normal forms. Another difference is that the  $\lambda$ -IAM does not

compute a code representation of the result  $\text{hnf}(t)$ . It only recovers the micro information  $\llbracket t \rrbracket_i$  about  $\text{hnf}(t)$  described above, by exploring the immutable code  $t$ . This is in accordance with other models: space-sensitive Turing machines do not compute the whole output but only single bits of it. The  $\lambda$ -IAM computes the spine of  $\text{hnf}(t)$ , that is, its sequence of head constructors, via potentially many runs. Indeed, if  $\text{hnf}(t) = \lambda x_1 \dots \lambda x_i. (y u_1 \dots u_j)$  then one has to execute the  $\lambda$ -IAM  $i + 1$  times, computing  $\llbracket t \rrbracket_k$  for  $0 \leq k \leq i$ . For  $k \in \{0, \dots, i - 1\}$ ,  $\llbracket t \rrbracket_k$  says that the  $\lambda$ -IAM terminates but not on a head normal form, thus providing the information that  $\text{hnf}(t)$  has  $i$  spine abstractions. The additional run provides the identity of the head variable (together with the information of whether it is bound or not) plus the number  $j$  of arguments.

*Terminating without Ever Succeeding.* The way in which the  $\lambda$ -IAM reflects  $\rightarrow_h$  divergence is subtle. It is possible that  $\rightarrow_h$  diverges on  $t$  and all the runs of the  $\lambda$ -IAM terminate on  $t$  without ever finding a head variable. The idea is that the  $\lambda$ -IAM approximates the head evaluation of  $t$ , computing  $\rightarrow_{h_i}$ , which corresponds to incrementally build the spine branch of the Lévy-Longo tree of  $t$ . For instance, on a non-terminating term such as  $\Lambda = (\lambda x. \lambda y. xx)(\lambda x. \lambda y. xx)$  the  $\lambda$ -IAM terminates at all depths. Note in fact that  $\Lambda \rightarrow_h \lambda x. \Lambda$ , i.e.  $\Lambda$  has an infinite number of head abstractions in its limit normal form. So  $\rightarrow_{h_i}$  terminates on  $\Lambda$  for every  $k$ , and, accordingly, so does the  $\lambda$ -IAM run at depth  $i$  (the value of  $\llbracket \Lambda \rrbracket_i$  is termination but not on a head normal form). On the contrary,  $\Omega$  has no head abstractions in its limit normal form and thus both  $\rightarrow_{h_i}$  and the  $\lambda$ -IAM diverge on  $\Omega$  at every depth  $i$ .

## 2.1 Improvements, Abstractly

To prove the soundness and adequacy theorems we shall use *improvements*, a refinement of the classical notion of bisimulation inspired by Sands [49], and explained here. The concrete improvements at work in the proofs of the theorems are defined in Sect. 8.

An improvement is a weak bisimulation between two transition systems preserving termination and guaranteeing that, whenever  $s$  and  $q$  are related and terminating, then  $q$  terminates in no more steps than  $s$ —the *no-more-steps* part implies that the definition is asymmetric in the way it treats the two transition systems, and it shall play a key role in the proof of adequacy.

*Preliminaries.* A deterministic transition system (DTS) is a pair  $\mathcal{S} = (S, \mathcal{T})$ , where  $S$  is a set of *states* and  $\mathcal{T} : S \rightarrow S$  a partial function. If  $\mathcal{T}(s) = s'$ , then we write  $s \rightarrow s'$ , and if  $s$  rewrites in  $s'$  in  $n$  steps then we write  $s \rightarrow^n s'$ . We note with  $\mathcal{F}_{\mathcal{S}}$  the set of final states, i.e. the subset of  $S$  containing all  $s \in S$  such that  $\mathcal{T}(s)$  is undefined. A state  $s$  is terminating if there exists  $n \geq 0$  and  $s' \in \mathcal{F}_{\mathcal{S}}$  such that  $s \rightarrow^n s'$ . We call  $S_{\downarrow}$  the set of terminating states of  $S$  and  $S_{\uparrow}$  stands for  $S \setminus S_{\downarrow}$ . The *evaluation length map*  $|\cdot| : S \rightarrow \mathbb{N} \cup \{\infty\}$  is defined as  $|s| := n$  if  $s \rightarrow^n s'$  and  $s' \in \mathcal{F}_{\mathcal{S}}$ , and  $|s| := \infty$  if  $s \in S_{\uparrow}$ .

*Definition 2.1 (Improvements).* Given two DTS  $\mathcal{S}$  and  $\mathcal{Q}$ , a relation  $\mathcal{R} \subseteq S \times Q$  is an *improvement* if given  $(s, q) \in \mathcal{R}$  the following conditions hold (for 2 and 3 see Fig. 1).

- (1) *Final state right:* if  $q \in \mathcal{F}_{\mathcal{Q}}$ , then  $s \rightarrow^n s'$ , for some  $s' \in \mathcal{F}_{\mathcal{S}}$  and  $n \geq 0$ .
- (2) *Transition left:* if  $s \rightarrow s'$ , then there exists  $s'', q', n, m$  such that  $s' \rightarrow^m s'', q \rightarrow^n q', s'' \mathcal{R} q'$  and  $n \leq m + 1$ .

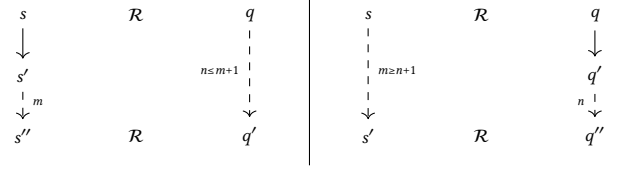


Figure 1: Diagrammatic definition of improvements.

- (3) *Transition right:* if  $q \rightarrow q'$ , then there exists  $s', q'', n, m$  such that  $s \rightarrow^m s', q' \rightarrow^n q'', s' \mathcal{R} q''$  and  $m \geq n + 1$ .

What improves along an improvement is the number of transitions required to reach a final state, if any.

PROPOSITION 2.2. *Let  $\mathcal{R}$  be an improvement on two DTS  $\mathcal{S}$  and  $\mathcal{Q}$ , and  $s \mathcal{R} q$ .*

- (1) Termination equivalence:  $s \in S_{\downarrow}$  if and only if  $q \in Q_{\downarrow}$ .
- (2) Improvement:  $|s| \geq |q|$ .

## 3 A GENTLE INTRODUCTION TO THE $\lambda$ -IAM

This section introduces the  $\lambda$ -IAM gradually. There are various mechanisms at work in the  $\lambda$ -IAM. Most of them are also part of the simpler machine that evaluates linear  $\lambda$ -terms (where each variable occurs exactly once). Then we first see the Linear  $\lambda$ -IAM, which is easier to grasp, and later refine it with non-linearity.

*Defining the Linear  $\lambda$ -IAM.* An essential point is that the initial code  $t$  of the machine never changes. The  $\lambda$ -IAM only moves over it, in a local way, with no rewriting of the code and without ever substituting terms for variables. The current position in the code  $t$  is represented as a pair  $(u, C)$  where  $C$  is a context (that is, a term with a hole) and  $C(u) = t$ .

A state  $s$  of the Linear  $\lambda$ -IAM has the shape  $(t, C, T, d)$  where  $(t, C)$  is a position while  $T$  and  $d$  are the linear token (which is a stack) and the direction, defined by:

$$\begin{aligned} \text{(LINEAR) TOKEN } T &::= \epsilon \mid @ \cdot T \mid \lambda \cdot T \mid x \cdot T \mid b \cdot T \\ \text{DIRECTION } d &::= \downarrow \mid \uparrow \end{aligned}$$

The token  $T$  can contain occurrences of  $@$  and  $\lambda$ , variables, and occurrences of  $b$ . The use of these symbols is explained below via examples. Directions  $\downarrow$  and  $\uparrow$ , pronounced *down* and *up*, shall be represented mostly via colors and underlining: the code term in **red** and underlined, for  $\downarrow$ , and the code context in **blue** and underlined, for  $\uparrow$ . This way, the fourth component of states is often omitted.

The transitions of the Linear  $\lambda$ -IAM are in Fig. 2. Roughly, when the direction is  $\downarrow$ , the machine looks for the head variable of  $t$ . The direction changes to  $\uparrow$  when the head variable  $x$  is found, moving also the machine to the position  $(\lambda x. u, D)$  of the binder of  $x$ . In direction  $\uparrow$ , the  $\lambda$ -IAM explores  $D$  looking for the argument that head evaluation would substitute on  $x$ . *Initial states* have the form  $s_{t,k} := (\underline{t}, \langle \cdot \rangle, @^k)$ , where  $t$  is a term and  $k \geq 0$  is the *depth*.

*Mechanism 1: Search Up to  $\beta$ -Redexes.* A basic mechanism of the Linear  $\lambda$ -IAM is the search of the head variable in direction  $\downarrow$  without recording  $\beta$ -redexes, via transitions  $\rightarrow_{@1}$  and  $\rightarrow_{@2}$ . Consider the following run at depth 0, with  $l_z := \lambda z.z$  and  $l_w := \lambda w.w$ .

Sub-tm	Context	Tok.	Sub-tm	Context	Tok.	Sub-tm	Context	Tok.	Sub-tm	Context	Tok.		
$tu$	$C$	$T$	$\rightarrow_{@1}$	$\underline{t}$	$C\langle\langle\cdot\rangle u\rangle$	$@\cdot T$	$t$	$C\langle\langle\cdot\rangle u\rangle$	$\lambda\cdot T$	$\rightarrow_{\lambda2}$	$tu$	$C$	$T$
$\lambda x.t$	$C$	$@\cdot T$	$\rightarrow_{@2}$	$\underline{t}$	$C\langle\lambda x.\langle\cdot\rangle\rangle$	$T$	$t$	$C\langle\lambda x.\langle\cdot\rangle\rangle$	$T$	$\rightarrow_{\lambda1}$	$\lambda x.t$	$C$	$\lambda\cdot T$
$\underline{x}$	$C\langle\lambda x.D\rangle$	$T$	$\rightarrow_{\text{var}}$	$\lambda x.D\langle x\rangle$	$C$	$x\cdot T$	$t$	$C\langle\langle\cdot\rangle u\rangle$	$x\cdot T$	$\rightarrow_{\text{arg}}$	$\underline{u}$	$C\langle t\langle\cdot\rangle\rangle$	$T$
$\lambda x.D\langle x\rangle$	$C$	$b\cdot T$	$\rightarrow_{\text{bt2}}$	$x$	$C\langle\lambda x.D\rangle$	$T$	$t$	$C\langle u\langle\cdot\rangle\rangle$	$T$	$\rightarrow_{\text{bt1}}$	$\underline{u}$	$C\langle\langle\cdot\rangle t\rangle$	$b\cdot T$

Figure 2: Linear  $\lambda$ -IAM transitions.

Sub-tm	Context	Tok.
$((\lambda y.\lambda x.xy)l_z)l_w$	$\langle\cdot\rangle$	$\epsilon \rightarrow_{@1}$
$(\lambda y.\lambda x.xy)l_z$	$\langle\cdot\rangle l_w$	$@ \rightarrow_{@1}$
$\lambda y.\lambda x.xy$	$\langle\langle\cdot\rangle l_z\rangle l_w$	$@\cdot@ \rightarrow_{@2}$
$\lambda x.xy$	$((\lambda y.\langle\cdot\rangle)l_z)l_w$	$@ \rightarrow_{@2}$
$xy$	$((\lambda y.\lambda x.\langle\cdot\rangle)l_z)l_w$	$\epsilon \rightarrow_{@1}$
$\underline{x}$	$((\lambda y.\lambda x.\langle\cdot\rangle)l_z)l_w$	$@$

When the machine faces an application, transition  $\rightarrow_{@1}$  records on the token the presence of an argument by pushing the symbol  $@$ , but not the argument itself (as it would instead do an environment machine), and moves to the left sub-term. Dually, on an abstraction, if the top of the token is  $@$  then the machine pops it and moves to the body of the abstraction. This way,  $\beta$ -redexes are simply skipped. Note indeed that after the first 4 transitions the machine has crossed 2  $\beta$ -redexes and the token is empty, as at the beginning.

*Mechanism 2: Finding Variables and Arguments.* In the example, the Linear  $\lambda$ -IAM finds the head variable  $x$ . Then transition  $\rightarrow_{\text{var}}$  applies, changing the direction to  $\uparrow$  and moving the position to the binder  $\lambda x$ . The machine now looks for the argument of the binder, exploring the context (now underlined and in blue) rather than the sub-term of the current position.

$\underline{x}$	$((\lambda y.\lambda x.\langle\cdot\rangle)l_z)l_w$	$@$	$\rightarrow_{\text{var}}$
$\lambda x.xy$	$(\lambda y.\langle\cdot\rangle)l_z$	$x\cdot@$	$\rightarrow_{\lambda1}$
$\lambda y.\lambda x.xy$	$\langle\langle\cdot\rangle l_z\rangle l_w$	$\lambda\cdot x\cdot@$	$\rightarrow_{\lambda2}$
$(\lambda y.\lambda x.xy)l_z$	$\langle\cdot\rangle l_w$	$x\cdot@$	$\rightarrow_{\text{arg}}$
$l_w$	$((\lambda y.\lambda x.xy)l_z)\langle\cdot\rangle$	$@$	

Note that  $\rightarrow_{\text{var}}$  adds  $x$  to the token, recording that the variable  $x$  has been found. The search for the argument is, again, *up to  $\beta$ -redexes*, via transitions  $\rightarrow_{\lambda1}$  and  $\rightarrow_{\lambda2}$ , adding a symbol  $\lambda$  to the token on abstractions and removing it on applications. The  $\uparrow$  phase ends with transition  $\rightarrow_{\text{arg}}$ , that fires when the hole of the context is facing an argument and the token contains a variable, here  $x$ —such an argument matches the binder  $\lambda x$  of the previously found variable. The transition removes  $x$  from the token, moves to the found argument, and switches direction.

*Mechanism 3: Backtracking.* On the example, the Linear  $\lambda$ -IAM continues by looking for the head variable of  $l_w$ , as expected.

$\lambda w.w$	$((\lambda y.\lambda x.xy)l_z)\langle\cdot\rangle$	$@$	$\rightarrow_{@1}$
$w$	$((\lambda y.\lambda x.xy)l_z)(\lambda w.\langle\cdot\rangle)$	$\epsilon$	$\rightarrow_{\text{var}}$
$\lambda w.w$	$(\lambda y.\lambda x.xy)l_z$	$w$	

Now something different happens. The machine is looking for the argument of  $l_w$ . Such an argument is not readily available, as the hole has no arguments in the current context, because  $\langle\cdot\rangle$  is the right sub-term of an application. Since  $l_w$  is a replacement for  $x$ , its argument is actually the argument  $y$  of  $x$ . Then the machine *backtracks* to  $x$ . Backtracking is started by transition  $\rightarrow_{\text{bt1}}$ , which

adds  $b$  to the token, moves to the left sub-term of the application, and changes direction. The next 3 steps look for  $\lambda x$  up to  $\beta$ -redexes:

$l_w$	$((\lambda y.\lambda x.xy)l_z)\langle\cdot\rangle$	$w$	$\rightarrow_{\text{bt1}}$
$(\lambda y.\lambda x.xy)l_z$	$\langle\cdot\rangle l_w$	$b\cdot w$	$\rightarrow_{@1}$
$(\lambda y.\lambda x.xy)$	$\langle\langle\cdot\rangle l_z\rangle l_w$	$@\cdot b\cdot w$	$\rightarrow_{@2}$
$\lambda x.xy$	$((\lambda y.\langle\cdot\rangle)l_z)l_w$	$b\cdot w$	$\rightarrow_{\text{bt2}}$
$x$	$((\lambda y.\lambda x.\langle\cdot\rangle)l_z)l_w$	$w$	$\rightarrow_{\text{arg}}$
$\underline{y}$	$((\lambda y.\lambda x.x\langle\cdot\rangle)l_z)l_w$	$\epsilon$	

When  $\lambda x$  is found, transition  $\rightarrow_{\text{bt2}}$  ends the backtracking, going back to the unique occurrence of  $x$  in the body of the abstraction (here linearity is crucial), and restores the search for arguments, changing direction and removing  $b$ . The next step is given by transition  $\rightarrow_{\text{arg}}$ , that finally finds the argument  $y$  of  $l_w$ , removing  $w$  from the token.

*Mechanism 4: Depth and Result.* In two more transitions, the Linear  $\lambda$ -IAM reaches a final state.

$\underline{y}$	$((\lambda y.\lambda x.x\langle\cdot\rangle)l_z)l_w$	$\epsilon$	$\rightarrow_{\text{var}}$
$(\lambda y.\lambda x.xy)$	$\langle\langle\cdot\rangle l_z\rangle l_w$	$y$	$\rightarrow_{\text{arg}}$
$l_z$	$((\lambda y.\lambda x.xy)\langle\cdot\rangle)l_w$	$\epsilon$	

Note that the machine terminates without finding the head variable  $z$  of the head normal form  $l_z$  of  $t := ((\lambda y.\lambda x.xy)l_z)l_w$ . This is because we executed the Linear  $\lambda$ -IAM at depth 0, while the head variable is at depth 1 in  $l_z$ . If we start over adding  $@$  to the initial token—corresponding to execute the Linear  $\lambda$ -IAM at depth 1—the run would go exactly as before but for the fact that the token of every state has an additional  $@$  at the end. The last state of the example would then no longer be final (since it has  $@$  on the token). The run at depth 1 has the following shape.

$((\lambda y.\lambda x.xy)l_z)l_w$	$\langle\cdot\rangle$	$@$	$\rightarrow_{@1}$
$\dots$	$\dots$	$\dots$	$\rightarrow^*\rightarrow_{\text{arg}}$
$\lambda z.z$	$((\lambda y.\lambda x.xy)\langle\cdot\rangle)l_w$	$@$	$\rightarrow_{@2}$
$\underline{z}$	$((\lambda y.\lambda x.xy)(\lambda z.\langle\cdot\rangle))l_w$	$\epsilon$	$\rightarrow_{\text{var}}$
$\dots$	$\dots$	$\dots$	$\rightarrow^*$
$((\lambda y.\lambda x.xy)l_z)l_w$	$\langle\cdot\rangle$	$z$	

After finding  $z$ , the machine does a sequence of transitions ending on the initial position with direction  $\uparrow$  and  $z$  on the token. The result of the run—that is, the identity of the head variable of the head normal form—is then available, on the token.

*Two Observations: @ and  $\lambda$  Can Be Merged, and Locality.* Note that the symbols  $@$  and  $\lambda$  of the token play dual roles and never interact. In the following sections, we then harmlessly merge them into a single symbol  $\bullet$ , pronounced *dot*.

Transitions  $\rightarrow_{\text{var}}$  and  $\rightarrow_{\text{bt2}}$  move from a variable to its binder and vice versa. These moves are local if one assumes that  $\lambda$ -terms are represented by implementing variable occurrences as pointers

to their binders, as in the proof net representation of  $\lambda$ -terms, see Sect. 10 for a precise comparison.

*Two Issues with Non-Linearity.* Generalizing the Linear  $\lambda$ -IAM to arbitrary, potentially non-linear  $\lambda$ -terms needs to address two difficulties. The first one is that, when the machine faces a sub-term  $\lambda x.t$  and needs to end backtracking via transition  $\rightarrow_{bt2}$ , it is no longer clear to which occurrence of  $x$  in  $t$  one should move, if there are many, or what to do, if  $x$  does not occur in  $t$ . The second issue is related to duplications. Consider the term  $t := (\lambda x.x(xu))(\lambda y.r)$ . The head evaluation of  $t$  makes two copies of  $\lambda y.r$  that are used differently, because one is applied to  $u$  and the other one to  $(\lambda y.r)u$ . The machine does not duplicate sub-terms but still it has to somehow distinguish different uses of a sub-term.

*Towards the  $\lambda$ -IAM.* The two issues are solved via three correlated modifications of the machine. First, the symbols  $x$  and  $b$  for the token are generalized to variable positions  $(x, \lambda x.C)$  inside the scope of their binder. Replacing  $b$  with  $(x, \lambda x.C)$  in particular removes the non-determinism on  $\rightarrow_{bt2}$ , when  $x$  has many occurrences<sup>4</sup>. Second, the token is split in two components, called *log* and *tape*. Roughly, the tape is the token of the Linear  $\lambda$ -IAM (generalized to variable positions) while the log stores, after transition  $\rightarrow_{arg}$ , the variable position for which the machine found the argument, so as to be able to know to which occurrence to backtrack to in transition  $\rightarrow_{bt1}$ . Third, there is a mechanism for distinguishing different uses of sub-terms. The log actually stores more than one variable position, and every position comes with its own log, acting as a sort of identifier for the use of that position. The next section formally develops this subtle mechanism. Logs and the way they distinguish uses of sub-terms without duplicating are far from being intuitive: they are both the mystery and the magic of the geometry of interaction.

## 4 THE $\lambda$ -IAM

In this section we introduce the data structures used by the  $\lambda$ -IAM and its transition rules.

*Terms and Leveled Contexts.* Let  $\mathcal{V}$  be a countable set of variables. Terms of the  $\lambda$ -calculus are defined as follows.

$$\lambda\text{-TERMS} \quad t, u, r ::= x \in \mathcal{V} \mid \lambda x.t \mid tu.$$

*Free and bound variables* are defined as usual:  $\lambda x.t$  binds  $x$  in  $t$ .

Terms are considered modulo  $\alpha$ -equivalence, and  $t\{x \leftarrow u\}$  denotes capture-avoiding (meta-level) substitution of all the free occurrences of  $x$  for  $u$  in  $t$ .

The study of the  $\lambda$ -IAM requires *contexts*, that are terms with a single occurrence of a special constant  $\langle \cdot \rangle$ , called *the hole*, that is a place-holder for a removed sub-term. In fact, we need a notion of context more informative than the usual one, introduced next.

$$\begin{array}{l} \text{LEVELED CONTEXTS} \\ C_0 ::= \langle \cdot \rangle \mid \lambda x.C_0 \mid C_0 t; \\ C_{n+1} ::= \lambda x.C_{n+1} \mid C_{n+1} t \mid t C_n. \end{array}$$

The index  $n$  in  $C_n$  counts the number of arguments into which the hole  $\langle \cdot \rangle$  is contained in  $C_n$ <sup>5</sup>. Contexts of level 0 are also called

<sup>4</sup>An invariant shall guarantee that one never backtracks inside an abstraction whose variable has no occurrences.

<sup>5</sup>Such an index has a natural interpretation in linear logic terms. According to the standard (call-by-name) translation of the  $\lambda$ -calculus into linear logic proof nets, in a context  $C_n$  the hole lies inside exactly  $n$  !-boxes.

*head contexts* and are denoted by  $H, K, G$ . The level of a context shall be omitted when not relevant to the discussion—note that any ordinary context can be written in a *unique way* as a leveled context, so that the omission is anyway harmless.

The *plugging*  $C_n \langle t \rangle$  of a term  $t$  in  $C_n$  is defined by replacing the hole  $\langle \cdot \rangle$  with  $t$ , potentially capturing free variables of  $t$ . Plugging  $C_n \langle C_m \rangle$  of a context for a context is defined similarly. A *position* (of level  $n$ ) in a term  $u$  is a pair  $(t, C_n)$  such that  $C_n \langle t \rangle = u$ .

*Logs and Logged Positions.* The  $\lambda$ -IAM relies on two mutually recursive notions, namely *logged positions* and *logs*: a logged position is a position  $(t, C_n)$  together with a  $\log^6$   $L_n$ , that is a list of logged positions, having length  $n$ .

$$\begin{array}{l} \text{LOGGED POSITIONS} \\ l ::= (t, C_n, L_n) \end{array} \qquad \begin{array}{l} \text{LOGS} \\ L_0 ::= \epsilon \quad L_{n+1} ::= l \cdot L_n \end{array}$$

We use  $\cdot$  also to concatenate logs, writing, e.g.,  $L_n \cdot L$ , using  $L$  for a log of unspecified length. Intuitively, logs contain some minimal information for backtracking to the associated position.

*Tape, Token, Direction, State.* The *tape*  $T$  is a finite sequence of elements of two kinds, namely logged positions, and occurrences of the special symbol  $\bullet$ , needed to cross abstractions and applications.

$$\text{TAPES} \quad T ::= \epsilon \mid \bullet \cdot T \mid l \cdot T.$$

A *token* is a log plus a tape.

*Definition 4.1 ( $\lambda$ -IAM State).* A state  $s$  of the  $\lambda$ -IAM is a quintuple  $(t, C, L, T, d)$  where  $t$  is a  $\lambda$ -term, called the *code term*,  $C$  is a context, called the *code context*,  $L$  is a log,  $T$  is a tape, and  $d$  is an element of  $\{\uparrow, \downarrow\}$ , called the *direction*.

As for the Linear  $\lambda$ -IAM, directions shall be represented mostly via colors and underlinings, omitting the fifth component.

*Initial States.* The  $\lambda$ -IAM starts on *initial states* of the form  $s_{t,k} := (t, \langle \cdot \rangle, \epsilon, \bullet^k, \downarrow)$ , where  $t$  is a  $\lambda$ -term<sup>7</sup>,  $k \geq 0$  is the *depth* of the state, and  $\epsilon$  is the empty log.

*Transitions.* The transitions of the  $\lambda$ -IAM are in Fig. 3. Their union is noted  $\rightarrow_{\lambda\text{IAM}}$ . A state  $s$  is *reachable* if  $s_{t,k} \rightarrow_{\lambda\text{IAM}}^* s$  for an initial state  $s_{t,k}$  and it is *final* if there exists no  $s'$  such that  $s \rightarrow_{\lambda\text{IAM}} s'$ . The shape of final states is characterized in Sect. 5.

As for the Linear  $\lambda$ -IAM,  $\downarrow$ -states  $(t, C, L, T)$  are queries about the head variable of (the head normal form of)  $t$  and  $\uparrow$ -states  $(t, C, L, T)$  are queries about the argument of an abstraction.

With respect to the Linear  $\lambda$ -IAM, transitions  $\rightarrow_{@1}$ ,  $\rightarrow_{@2}$ ,  $\rightarrow_{\lambda1}$ , and  $\rightarrow_{\lambda2}$  are respectively renamed  $\rightarrow_{\bullet1}$ ,  $\rightarrow_{\bullet2}$ ,  $\rightarrow_{\bullet4}$ , and  $\rightarrow_{\bullet3}$ , because  $\bullet$  subsumes both token symbols  $@$  and  $\lambda$ . The role of both symbols  $x$  and  $b$  is instead played by logged positions. Note that transition  $\rightarrow_{arg}$  moves the logged position from the tape to the log, and that transition  $\rightarrow_{bt1}$  moves it back to the tape, as it shall specify, at the end of the backtracking, to which variable occurrence  $\rightarrow_{bt2}$  has to move. The less intuitive aspect of the  $\lambda$ -IAM is the splitting of the log in transition  $\rightarrow_{var}$  and the dual concatenation of logs in

<sup>6</sup>In computer science logs are traces that can only grow, while here they also shrink. The terminology suggests a tracing mechanism—*trace* is avoided because related to categorical formulations of the GoI.

<sup>7</sup>To be precise, one needs a *well-named* term, that is, one in which all bound variables have distinct names, also distinct wrt free names. Since the code is immutable, this detail is only needed to assure that the relationship between variables and binders is unambiguous, for the definition of the transitions.

Sub-term	Context	Log	Tape		Sub-term	Context	Log	Tape
$tu$	$C$	$L$	$T$	$\rightarrow_{\bullet 1}$	$\underline{t}$	$C\langle\langle\cdot\rangle u\rangle$	$L$	$\bullet \cdot T$
$\lambda x.t$	$C$	$L$	$\bullet \cdot T$	$\rightarrow_{\bullet 2}$	$\underline{t}$	$C\langle\lambda x.\langle\cdot\rangle\rangle$	$L$	$T$
$x$	$C\langle\lambda x.D_n\rangle$	$L_n \cdot L$	$T$	$\rightarrow_{\text{var}}$	$\lambda x.D_n\langle x\rangle$	$\underline{C}$	$L$	$(x, \lambda x.D_n, L_n) \cdot T$
$\lambda x.D_n\langle x\rangle$	$C$	$L$	$(x, \lambda x.D_n, L_n) \cdot T$	$\rightarrow_{\text{bt}2}$	$x$	$\underline{C\langle\lambda x.D_n\rangle}$	$L_n \cdot L$	$T$
$t$	$\underline{C\langle\langle\cdot\rangle u\rangle}$	$L$	$\bullet \cdot T$	$\rightarrow_{\bullet 3}$	$tu$	$\underline{C}$	$L$	$T$
$t$	$\underline{C\langle\lambda x.\langle\cdot\rangle\rangle}$	$L$	$T$	$\rightarrow_{\bullet 4}$	$\lambda x.t$	$\underline{C}$	$L$	$\bullet \cdot T$
$t$	$\underline{C\langle\langle\cdot\rangle u\rangle}$	$L$	$l \cdot T$	$\rightarrow_{\text{arg}}$	$\underline{u}$	$C\langle t\langle\cdot\rangle\rangle$	$l \cdot L$	$T$
$t$	$\underline{C\langle u\langle\cdot\rangle\rangle}$	$l \cdot L$	$T$	$\rightarrow_{\text{bt}1}$	$\underline{u}$	$C\langle\langle\cdot\rangle t\rangle$	$L$	$l \cdot T$

Figure 3:  $\lambda$ -IAM transitions.

$\rightarrow_{\text{bt}2}$ . To justify it, note first that the log is extended every time the machine enters into an argument via  $\rightarrow_{\text{arg}}$ , which is also when the level of the position increases of 1. Note also that transitions  $\rightarrow_{\text{bt}1}$  moves out of an argument (decreasing the level of 1) and removes an entry from the log. To preserve the invariant that the log length is exactly the depth of the context, transition  $\rightarrow_{\text{var}}$  splits the log between the two positions  $(\lambda x.D_n\langle x\rangle, C)$  and  $(x, \lambda x.D_n)$ , according to the depth of their contexts, and  $\rightarrow_{\text{bt}2}$  merges them back.

## 5 PROPERTIES OF THE $\lambda$ -IAM

Here we first discuss a few invariants of the data structures of the machine, and then we analyze final states and the semantic interpretation defined by the  $\lambda$ -IAM.

*The Code Invariant.* An inspection of the rules shows that, along a computation, the machine travels on a  $\lambda$ -term without altering it.

PROPOSITION 5.1 (CODE INVARIANT). *If  $(t, C, L, T, d) \rightarrow_{\lambda\text{IAM}} (u, D, L', T', d')$ , then  $C\langle t\rangle = D\langle u\rangle$ .*

*The Balance Invariant.* Given a state  $(t, C, L, T, d)$ , the log and the tape, i.e. the token, verify two easy invariants connecting them to the position  $(t, C)$  and the direction  $d$ . The log  $L$ , together with the position  $(t, C)$ , form a logged position, i.e. the length of  $L$  is exactly the level of the code context  $C$ . This fact guarantees that the  $\lambda$ -IAM never gets stuck because the log is not long enough for transitions  $\rightarrow_{\text{var}}$  and  $\rightarrow_{\text{bt}1}$  to apply.

About the tape, note that every time the machine switches from a  $\downarrow$ -state to an  $\uparrow$ -state (or vice versa), a logged position is pushed (or popped) from the tape  $T$ . Thus, for reachable states, the number of logged positions in  $T$  gives the direction of the state. These intuitions are formalized by the balance invariant below. Given a direction  $d$  we use  $d^n$  for the direction obtained by switching  $d$  exactly  $n$  times (i.e.,  $\downarrow^0 = \downarrow, \uparrow^0 = \uparrow, \downarrow^{n+1} = \uparrow^n$  and  $\uparrow^{n+1} = \downarrow^n$ ).

LEMMA 5.2 (BALANCE INVARIANT). *Let  $s = (t, C_n, L, T, d)$  be a reachable state and  $|T|_l$  the number of logged positions in  $T$ . Then*

- (1) Position and log:  $(t, C_n, L)$  is a logged position, and
- (2) Tape and direction:  $d = \downarrow^{|T|_l}$ .

Note that, because of the invariant, the tape  $T$  of a reachable  $\uparrow$ -state always contains at least one logged position, which is why it can be seen as the answer to a query about the head variable. More generally, the parity of a logged position  $l$  on the tape determines

the role of  $l$ . If  $l$  is the  $n$ -th position on  $T$  (from the right) and  $n$  is odd, then  $l$  was added by  $\rightarrow_{\text{var}}$  and denotes a found variable waiting for an argument, while if  $n$  is even then  $l$  was added by  $\rightarrow_{\text{bt}1}$ , its argument was already found, and the machine is backtracking to  $l$ .

*The Exhaustible State Invariant.* The study of the  $\lambda$ -IAM requires to prove that some bad configurations never arise. On states such as  $(\lambda x.D\langle x\rangle, C, L, l \cdot T)$ , transition  $\rightarrow_{\text{bt}2}$  requires the logged position  $l$  to have shape  $(x, \lambda x.D, L')$ , that is, to contain a position isolating an occurrence of  $x$  in  $\lambda x.D\langle x\rangle$ , otherwise the machine is stuck. The *exhaustible state invariant* guarantees that the machine never gets stuck for this reason. The invariant being technical, it is developed in the Section 7. Here we only mention a key consequence.

PROPOSITION 5.3 (LOGGED POSITIONS NEVER BLOCK THE  $\lambda$ -IAM). *Let  $(\lambda x.D\langle x\rangle, C, L, l \cdot T)$  be a reachable state. Then  $l = (x, \lambda x.D, L')$ .*

*Reversibility.* The proof of Prop. 5.3 relies on a key property of the  $\lambda$ -IAM, that is, bi-determinism, or reversibility: the machine is deterministic, and moreover for each state  $s$  there is at most one state  $s'$  such that  $s' \rightarrow_{\lambda\text{IAM}} s$ . The property follows by simply inspecting the rules. Moreover, a run can be reverted by just switching the direction.

PROPOSITION 5.4 (REVERSIBILITY). *If  $(t, C, L, T, d) \rightarrow_{\lambda\text{IAM}} (u, D, L', T', d')$ , then  $(u, D, L', T', d') \rightarrow_{\lambda\text{IAM}} (t, C, L, T, d)$ .*

*Final States.* A run of initial state  $s_{t,k} = (\underline{t}, \langle\cdot\rangle, \epsilon, \bullet^k)$  may either never stop or end in one of three possible final states, as explained below. We shall prove this fact in Section 7 (Lemma 7.7). In order to explain the idea, let  $\lambda x_0 \dots \lambda x_i.(y u_1 \dots u_j)$  be the head normal form  $\text{hnf}(t)$  of  $t$ . The three cases are:

- *Mute termination*  $(\lambda x.u, C, L, \epsilon)$ : this is the machine's way of saying that  $i > k$ , that is, the run from  $s_{t,k}$  (which allows to go under  $k$  abstractions) terminates but  $\text{hnf}(t)$  has more than  $k$  head abstractions, and so finding the head variable requires to increase the depth.
- *Open success*  $(\underline{y}, C, L, \bullet^j)$ : the machine has found the head variable, and it is the *free* variable  $y$ , which has  $j$  arguments and is under less than  $k$  head abstractions. Note that if  $y$  is instead bound by a  $\lambda$ -abstraction, then the machine is not stuck, as the machine would do a  $\rightarrow_{\text{var}}$  transition (as guaranteed by the balance invariant).

- *Bound success*  $(t, \langle \cdot \rangle, L, \bullet^m \cdot l \cdot \bullet^j)$ : the head variable has been found and it is  $y = x_m$ , to which  $j$  arguments are applied. When the machine  $\downarrow$ -travels on the head variable  $y$ , and it is abstracted, the logged position  $l$  containing  $x_m$  is put on the tape and the direction switches—the answer has been found. The sequence  $\bullet^m$  on top of tape in the final state comes from the  $\uparrow$  backtracking along the spine of  $\text{hnf}(t)$  for the equivalent of  $m$  abstractions, each one adding one  $\bullet$ . At this point the  $\lambda$ -IAM stops. Thus the abstraction binding  $y$  is  $\lambda x_m$ , i.e.  $y = x_m$ .

*The Semantics.* The characterization of final states induces a semantic interpretation of terms, that we shall show to be sound and adequate with respect to (linear) head evaluation.

*Definition 5.5 ( $\lambda$ -IAM Semantics).* We define the  $\lambda$ -IAM semantics of  $\lambda$ -terms by way of a family of functions  $\llbracket \cdot \rrbracket_k : \Lambda \rightarrow (\mathbb{N} \times \mathbb{N}) \cup (\mathcal{V} \times \mathbb{N}) \cup \{\downarrow, \perp\}$ , where  $k \in \mathbb{N}$ , defined as follows.

$$\llbracket t \rrbracket_k := \begin{cases} \langle h, j \rangle & \text{if } s_{t,k} \xrightarrow{*}_{\lambda\text{IAM}} (t, \langle \cdot \rangle, \epsilon, \bullet^h \cdot l \cdot \bullet^j), \\ \langle x, h \rangle & \text{if } s_{t,k} \xrightarrow{*}_{\lambda\text{IAM}} (\underline{x}, C, L, \bullet^h), \\ \downarrow & \text{if } s_{t,k} \xrightarrow{*}_{\lambda\text{IAM}} (\lambda x.u, C, L, \epsilon), \\ \perp & \text{if the } \lambda\text{-IAM diverges on } s_{t,k}. \end{cases}$$

## 5.1 Further Properties

The following properties of the  $\lambda$ -IAM are required for the proofs but are not essential for a first understanding of its functioning, so we suggest to skip them at a first reading.

*Lifting.* The  $\lambda$ -IAM verifies a sort of context-freeness with respect to the tape  $T$ . Intuitively, *lifting* the tape preserves the shape of the run and of the final state (up to lifting).

**LEMMA 5.6 (LIFTING).** *If  $(t, C, L, T, d) \xrightarrow{n}_{\lambda\text{IAM}} (u, D, L', T', d')$ , then  $(t, C, L, T \cdot T'', d) \xrightarrow{n}_{\lambda\text{IAM}} (u, D, L', T' \cdot T'', d')$ .*

*Monotonicity of Runs.* The previous lemma states that lifting the input from  $\bullet^k$  to  $\bullet^{k+1}$  cannot decrease the length of the  $\lambda$ -IAM run. Next, we show that if the run of input  $\bullet^k$  is successful then the run of input  $\bullet^{k+1}$  is also successful, in the same way, and it has the same length. As a consequence, the length may increase only if the run on  $\bullet^k$  mutely terminates.

We write  $|t|_k$  for the length of the  $\lambda$ -IAM run of initial state  $s_{t,k} := (t, \langle \cdot \rangle, \epsilon, \bullet^k)$ , that is for the length of the maximum sequence of transitions from  $s_{t,k}$ , if the  $\lambda$ -IAM terminates, and  $|t|_k = \infty$  if the machine diverges. The next lemma compares run lengths, for which we consider that  $i < \infty$  for every  $i \in \mathbb{N}$  and  $\infty \not< \infty$ . We also write  $s_{t,k}^n$  for the state such that  $s_{t,k} \xrightarrow{n}_{\lambda\text{IAM}} s_{t,k}^n$ , if it exists.

**LEMMA 5.7 (MONOTONICITY OF RUNS).** *The length of runs cannot decrease if the input increases, that is,  $|t|_k \leq |t|_{k+1}$ . Moreover, if  $|t|_k = n \in \mathbb{N}$  and the final state  $s_{t,k}^n$  is bound (resp. open) successful then  $|t|_k = |t|_h$  for every  $h > k$  and the final state  $s_{t,h}^n$  is bound (resp. open) successful.*

## 6 MICRO-STEP REFINEMENT

The proof of soundness of the  $\lambda$ -IAM cannot be directly carried out with respect to head evaluation: this is specified using meta-level substitutions, here noted  $t\{x \leftarrow u\}$ , which is a macro operation,

potentially making *many* copies of  $u$  and modifying  $t$  in *many* places, while the  $\lambda$ -IAM does a minimalistic evaluation that in general does not even pass through most of those many places. It is very hard—if possible at all—to define explicitly a bisimulation of  $\lambda$ -IAM runs (as required for soundness) that relates states whose code is modified by meta-level substitution.

We then switch to *linear* head evaluation (shortened to LHE), a refinement of head evaluation in which substitution is performed in *micro-steps*, replacing only the head variable occurrence, and keeping the substitution suspended for all the other occurrences. This is also the approach followed by Danos, Herbelin, and Regnier [25].

We depart from their approach, however, in the way we formally define LHE. We adopt a formulation where the suspension of the substitution is formalized via a sharing constructor  $t[x \leftarrow u]$ , which is nothing else but a compact notation for let  $x = u$  in  $t$ , and the rewriting is modified accordingly. They instead avoid sharing, by encoding  $t[x \leftarrow u]$  as  $(\lambda x.t)u$ , which is more compact but conflates different concepts and makes the technical development less clean.

An important point is that head evaluation and its linear variant are observationally equivalent, that is, one terminates on  $t$  if and only if the other terminates on  $t$ , and they produce the same head variable, as we shall discuss below.

*The Adopted Presentation.* Linear head evaluation was introduced by Mascari & Pedicini and Danos & Regnier [28, 43] as a strategy on proof nets. It is to proof nets for the  $\lambda$ -calculus what head evaluation is to the  $\lambda$ -calculus. The presentation adopted here, noted  $\rightarrow_{\text{lh}}$ , was introduced by Accattoli [2], formulated as a strategy in a  $\lambda$ -calculus with explicit sharing, the *linear substitution calculus*<sup>8</sup> (shortened to LSC). The LSC presentation of  $\rightarrow_{\text{lh}}$  is isomorphic to the one on proof nets [4], while the one used by Danos and Regnier—although closely related to proof nets—is not. It is isomorphic only up to Regnier's  $\sigma$ -equivalence [48].

*LSC Terms and Leveled contexts.* Let  $\mathcal{V}$  be a countable set of variables. Terms of the *linear substitution calculus* (LSC) are defined by the following grammar.

$$\text{LSC TERMS} \quad t, u, r ::= x \in \mathcal{V} \mid \lambda x.t \mid tu \mid t[x \leftarrow u].$$

The construct  $t[x \leftarrow u]$  is called an *explicit substitution* or ES, not to be confused with meta-level substitution  $t\{x \leftarrow u\}$ . As is standard,  $t[x \leftarrow u]$  binds  $x$  in  $t$ , but not in  $u$ —terms are still considered up to  $\alpha$ -conversion. Leveled contexts naturally extend to the LSC.

**LEVELED CONTEXTS**

$$\begin{aligned} C_0 & ::= \langle \cdot \rangle \mid \lambda x.C_0 \mid C_0 t \mid C_0[x \leftarrow t]; \\ C_{n+1} & ::= \lambda x.C_{n+1} \mid C_{n+1} t \mid C_{n+1}[x \leftarrow t] \mid tC_n \mid t[x \leftarrow C_n]. \end{aligned}$$

*Contexts and Plugging.* The LSC makes a crucial use of contexts to define its operational semantics. First of all, we need *substitution contexts*, that simply pack together ES:

$$\text{SUBSTITUTION CONTEXTS} \quad S ::= \langle \cdot \rangle \mid S[x \leftarrow t].$$

When plugging is used for substitution contexts, we write it in a post-fixed manner, that is  $\langle t \rangle S$ , to stress that the ES actually appears on the right of  $t$ .

<sup>8</sup>The LSC is a subtle reformulation of Milner's calculus with explicit substitutions [37, 46], inspired by Accattoli and Kesner structural  $\lambda$ -calculus [11].



RULES AT TOP LEVEL		CONTEXTUAL CLOSURE		NOTATION	
$\langle \lambda x.t \rangle Su$	$\mapsto_{\text{dB}}$	$\langle t[x \leftarrow u] \rangle S$			
$H\langle x \rangle[x \leftarrow t]$	$\mapsto_{\text{ls}}$	$H\langle t \rangle[x \leftarrow t]$	$\frac{t \mapsto_a u}{H\langle t \rangle \rightarrow_a H\langle u \rangle}$	$a \in \{\text{dB}, \text{ls}, \text{gc}\}$	$\rightarrow_{\text{lh}} := \rightarrow_{\text{dB}} \cup \rightarrow_{\text{ls}} \cup \rightarrow_{\text{gc}}$
$t[x \leftarrow u]$	$\mapsto_{\text{gc}}$	$t$ if $x \notin \text{fv}(t)$			

Figure 4: Rewriting rules for linear head evaluation  $\rightarrow_{\text{lh}}$ .

Sub-term	Context	Log	Tape	Sub-term	Context	Log	Tape
$t[x \leftarrow u]$	$C$	$L$	$T$	$\rightarrow_{\text{es}}$	$\underline{t}$	$C\langle \langle \cdot \rangle [x \leftarrow u] \rangle$	$L$
$\underline{x}$	$C\langle D_n[x \leftarrow u] \rangle$	$L_n \cdot L$	$T$	$\rightarrow_{\text{var2}}$	$\underline{u}$	$C\langle D_n\langle x \rangle[x \leftarrow \langle \cdot \rangle] \rangle$	$(x, D_n[x \leftarrow u], L_n) \cdot L$
$t$	$C\langle \langle \cdot \rangle [x \leftarrow u] \rangle$	$L$	$T$	$\rightarrow_{\text{es2}}$	$t[x \leftarrow u]$	$\underline{C}$	$L$
$u$	$C\langle D_n\langle x \rangle[x \leftarrow \langle \cdot \rangle] \rangle$	$(x, D_n[x \leftarrow u], L_n) \cdot L$	$T$	$\rightarrow_{\text{var3}}$	$x$	$C\langle D_n[x \leftarrow u] \rangle$	$L_n \cdot L$

Figure 5: Transitions for LSC-terms.

*Linear Head Evaluation.* The LSC comes with a notion of reduction that resembles the decomposed, micro-step process of cut-elimination in linear logic proof-nets. Essentially, the meta-level substitution  $t\{x \leftarrow u\}$  is decomposed into a sequence replacements from  $t[x \leftarrow u]$  of one occurrence of  $x$  in  $t$  with  $u$  at the time. Linear head evaluation, moreover, is the reduction that replaces only the head variable occurrence  $y$ , if it is bound by an ES  $[y \leftarrow r]$  and leaves the other occurrences of  $y$ , if any, bound by  $[y \leftarrow r]$ .

The rewriting rules, in Figure 4, are first defined at top level and then closed by head contexts. A feature of the LSC is that contexts are also used to define the linear substitution rule at top level  $\mapsto_{\text{ls}}$ . In plugging  $t$  in  $H$ , rule  $\mapsto_{\text{ls}}$  may perform on-the-fly renaming of bound variables in  $H$ , to avoid capture of free variables of  $t$ . Often, the literature does not include rule  $\mapsto_{\text{gc}}$ , responsible for erasing steps, in the definition of  $\rightarrow_{\text{lh}}$ . The reason is that  $\mapsto_{\text{gc}}$  is strongly normalizing and it can be postponed. Note that our definition of  $\rightarrow_{\text{lh}}$  is non-deterministic, for instance  $t := (\lambda x.(y[y \leftarrow u]))r \rightarrow_{\text{ls}} (\lambda x.(u[y \leftarrow u]))r$  and  $t \rightarrow_{\text{dB}} x[y \leftarrow u][x \leftarrow r]$ . It is not a problem, as  $\rightarrow_{\text{lh}}$  has the diamond property—this is standard, see [2].

*Example 6.1.* We provide here an example of LHE sequence. Consider the following 3 steps:

$$\begin{aligned} (\lambda x.xx)(\lambda y.y) &\rightarrow_{\text{dB}} (xx)[x \leftarrow \lambda y.y] \rightarrow_{\text{ls}} ((\lambda y.y)x)[x \leftarrow \lambda y.y] \\ &\rightarrow_{\text{dB}} y[y \leftarrow x][x \leftarrow \lambda y.y] \end{aligned}$$

that turn a  $\beta$ /multiplicative redex into a ES, substitute on the head variable occurrence, and continue with another multiplicative step. Two linear substitution steps on the head, followed by two steps of garbage collection complete the evaluation:

$$\begin{aligned} y[y \leftarrow x][x \leftarrow \lambda y.y] &\rightarrow_{\text{ls}} x[y \leftarrow x][x \leftarrow \lambda y.y] \\ &\rightarrow_{\text{ls}} (\lambda y.y)[y \leftarrow x][x \leftarrow \lambda y.y] \rightarrow_{\text{gc}}^2 \lambda z.z \end{aligned}$$

*Additional  $\lambda$ -IAM transitions.* The  $\lambda$ -IAM presented in the previous sections is easily adapted to the LSC, by simply considering (logged) positions with respect to the extended syntax, and adding the 4 transitions for ES in Fig. 5.

Transitions  $\rightarrow_{\text{es}}$  and  $\rightarrow_{\text{es2}}$  simply skip ES during search—now search is *up to  $\beta$ -redexes and ES*. Transition  $\rightarrow_{\text{var2}}$  shortcuts the search of the term  $u$  to substitute for  $x$ , given that  $u$  is already available in  $[x \leftarrow u]$ . Therefore, the machine stays in the  $\downarrow$  phase and moves to evaluate  $u$ . Note that the logged position for  $x$  is directly added to the log and not to the tape. This is because we have

avoided the search of the argument. We have reached it directly: note that when a  $\uparrow$ -search ends with the  $\rightarrow_{\text{arg}}$  transition, the logged position indeed goes from the tape to the log. Transition  $\rightarrow_{\text{var3}}$  is dual to  $\rightarrow_{\text{var2}}$ , and it is used to keep looking for arguments when the current subterm  $u$  has none left.

All results and considerations of Sect. 4 and 5 still hold in this more general setting, *mutatis mutandis*.

*Relationship with Head Evaluation, and Normal Forms.* We shall prove that the  $\lambda$ -IAM is sound and adequate with respect to linear head evaluation  $\rightarrow_{\text{lh}}$ , by showing that it approximates the spine structure of the  $\rightarrow_{\text{lh}}$ -normal form of  $t$ , when it exists. Let us explain why the same holds also for head evaluation. The relationship between linear head and head evaluation is studied in detail by Accattoli and Dal Lago [12], who prove that the two notions are termination equivalent and produce the same normal forms up to unfolding. The definition of unfolding  $t \downarrow$  of a term with ES follows.

$$\begin{aligned} x \downarrow &:= x & (tu) \downarrow &:= t \downarrow u \downarrow \\ (\lambda x.t) \downarrow &:= \lambda x.t \downarrow & (t[x \leftarrow u]) \downarrow &:= t \downarrow \{x \leftarrow u \downarrow\} \end{aligned}$$

**PROPOSITION 6.2** ([12]). *Let  $t$  be a  $\lambda$ -term. There exists a head evaluation  $t \rightarrow_{\text{h}}^* \text{hnf}(t)$  to head normal form if and only if there exists a linear head evaluation  $t \rightarrow_{\text{lh}}^* \text{lhnf}(t)$  to linear head normal form. Moreover,  $\text{lhnf}(t) \downarrow = \text{hnf}(t)$ .*

Additionally,  $\text{lhnf}(t)$  and  $\text{hnf}(t)$  have the same spine structure. A linear head normal form has the same shape  $\lambda x_1. \dots \lambda x_k.(y t_1 \dots t_h)$  of a head normal form but for the fact that each spine sub-term may be surrounded by a substitution context  $S$ , that is, they have the cumbersome shape (where  $S_i$  surrounds  $\lambda x_i. \dots \lambda x_k.(y t_1 \dots t_h)$  and  $S'_j$  surrounds  $y t_1 \dots t_j$ ):

$$\langle \lambda x_1. \langle \lambda x_2. \dots \langle \lambda x_k. (\langle \langle y \rangle t_1 \rangle S'_1 \dots t_h) S'_h \rangle S_k \dots \rangle S_2 \rangle S_1 \rangle$$

where none of the ES in  $S_i$  and  $S'_j$  binds  $y$  (otherwise there would be a  $\mapsto_{\text{ls}}$  redex)<sup>9</sup>.

It is fairly evident that if  $t$  is  $\mapsto_{\text{lh}}$  normal then  $t$  and  $t \downarrow$  have the same spine structure (that is, the number of head abstractions, the identity of the head variable, and the number of its arguments): the only substitutions that may alter the spine are those acting on the

<sup>9</sup>An inductive characterization of linear head normal forms can be given along the lines of the one in Figure 11 at page 36 of [9]. The minor difference is that in that paper  $\rightarrow_{\text{lh}}$  does not include  $\rightarrow_{\text{gc}}$ .

head variable, but  $t \downarrow$  unfolds no such substitutions, because no ES of  $t$  can bound the head variable, since  $t$  is  $\rightarrow_{\text{IS}}$  normal. In the following, we shall then consider a  $\rightarrow_{\text{IH}}$ -normal LSC term  $t$  and without loss of generality consider its shape to be  $\lambda x_1 \dots \lambda x_k. (y t_1 \dots t_n)$ , that is, as if it were a  $\rightarrow_{\text{IH}}$ -normal  $\lambda$ -term.

## 7 THE EXHAUSTIBLE STATE INVARIANT

The previous sections introduced all the ingredients for the formal study of the  $\lambda$ -IAM. From now on, we turn to the development of the proofs of soundness and adequacy. The first step, taken here, is to formalize the exhaustible state invariant mentioned in Sect. 5.

The intuition behind the invariant is that whenever a logged position  $l$  occurs in a reachable state, it is there *for a reason*: no logged position occurs in initial states, and transitions only add logged positions to which the machine may come back. In particular, if the state is set in the right way (to be explained), the  $\lambda$ -IAM can reach  $l$ , *exhausting* it.

*Why It Is Needed.* The exhaustible state invariant is meant to show that some undesirable configurations never arise, to characterize the final states of the  $\lambda$ -IAM. On states such as  $(\lambda x. D(x), C, L, l \cdot T)$  the  $\lambda$ -IAM requires the logged position  $l$  to have shape  $(x, \lambda x. D, L')$ , that is, to be associated to a position isolating an occurrence of  $x$  in  $\lambda x. D(x)$ , otherwise the machine is stuck. Similarly, on states such as  $(t, C \langle D(x) [x \leftarrow \langle \cdot \rangle] \rangle, l \cdot L, T)$  the position of  $l$  is expected to isolate an occurrence of  $x$  in  $D(x)$ , or the machine is stuck. Luckily, the machine is never stuck for these reasons, and exhaustible states are the technical tool to prove it<sup>10</sup>. The invariant also allows to prove that the backtracking to a logged position  $l$  always ends on a state of position  $l$ , as expected—that is, backtracking always succeeds, and to characterize the structure of the tape in final states.

*Preliminaries.* Exhaustible states rest on some *tests* for their logged positions. More specifically, each logged position  $l$  in a state  $s$  has an associated test state  $s_l$  that tunes the data structures of  $s$  as to test for the reachability of  $l$ . Actually, there shall be two classes of test states, one accounting for the logged positions in the tape of  $s$ , and one for those in the log of  $s$ .

*Tape Tests.* Tape tests are easy to define. They focus on one of the logged positions in the tape, discarding everything that follows.

*Definition 7.1 (Tape tests).* Let  $s = (t, C, L, T' \cdot l \cdot T'', d)$  be a state. Then the *tape test* of  $s$  of focus  $l$  is the state  $s_l = (t, C, L, T' \cdot l, \uparrow^{|T' \cdot l|} l)$ .

Note that the direction of tape tests is reversed with respect to what stated by the *tape and direction* invariant (Lemma 5.2), and so, in general, they are not reachable states. Such a counter-intuitive fact is needed for the invariant to go through, no more no less. When proving that backtracking always succeeds (Lemma 7.6 below), we shall extend their tape via the tape lifting property (Lemma 5.6) as to satisfy the invariant and be reachable.

*Log Tests.* The idea is the same underlying tape tests: they focus on a given logged position in the log. Their definition however requires more than simply stripping down the log, as the new log

<sup>10</sup> One could redefine the transitions of the  $\lambda$ -IAM asking—for these states—to jump to whatever variable position is in the logged position  $l$ . Then the  $\lambda$ -IAM would not get stuck, and the invariant would not be needed for characterizing final states, but we would then need it for soundness—there is no easy way out.

and the state position still have to form a logged position, which requires to change the state position—said differently, the *position and the log* invariant (Lemma 5.2) has to be preserved.

In the applications of the exhaustible invariant given below, we need only log tests of a very simple form. Namely, given a state  $s = (t, \underline{C}, l \cdot L, T)$ , we shall consider the log test  $s_l := (t, \underline{C}, l \cdot L, \epsilon)$ , obtained by emptying the tape and (in this case) without changing the position. The more general form of log tests needing the position change is technical and defined at the end of the section—it is unavoidable for proving the invariant, but we fear that giving it here would obfuscate the use of the exhaustible invariant, whose idea is instead quite simple.

*The Exhaustibility Invariant.* Exhausting a logged position  $l$  means backtracking to it. We then decorate the backtracking transition  $\rightarrow_{\text{bt}1}$  and  $\rightarrow_{\text{bt}2}$  as  $\rightarrow_{\text{bt}1, l}$  and  $\rightarrow_{\text{bt}2, l}$  to specify the involved logged position  $l$ . We also need a notion of state positioned in  $l$ , which is the target state of  $\rightarrow_{\text{bt}2, l}$ .

*Definition 7.2 (State surrounding a position).* Let  $l = (t, D, L')$  be a logged position. A state  $s$  surrounds  $l$  if  $s = (t, \underline{C}_n \langle D \rangle, L' \cdot L_n, \epsilon)$  for some context  $C_n$  and log  $L_n$ .

After having introduced all the necessary preliminaries, we can define exhaustible states.

*Definition 7.3 (Exhaustible States).*  $\mathcal{E}$  is the smallest set of states  $s$  such that if  $s_l$  is a tape or a log test of  $s$ , then  $s_l \xrightarrow{*}_{\lambda\text{IAM}} \rightarrow_{\text{bt}2, l} s'' \in \mathcal{E}$ , where  $s''$  surrounds  $l$ . States in  $\mathcal{E}$  are called *exhaustible*.

Informally, exhaustible states are those for which every logged position can be successfully tested, that is, the  $\lambda$ -IAM can backtrack to (an exhaustible state surrounding) it, if properly initialized. Roughly, a state is exhaustible if the backtracking information encoded in its logged positions is coherent. The set  $\mathcal{E}$  being the *smallest* set of such states implies that checking that a state is exhaustible can be finitely certified, *i.e.* there must be a finitary proof.

**PROPOSITION 7.4 (EXHAUSTIBLE INVARIANT).** *Let  $s$  be a  $\lambda$ -IAM reachable state. Then  $s$  is exhaustible.*

The proof of Prop. 7.4 is long, but logically quite simple, being structured around a simple induction on the length of the run from the initial state to  $s$ , and can be found in the Extended Version of this paper [13].

*Consequences of the Exhaustible Invariant.* First, the  $\lambda$ -IAM never gets stuck for a mismatch of logged positions.

**COROLLARY 7.5 (LOGGED POSITIONS NEVER BLOCK THE  $\lambda$ -IAM).** *Let  $s$  be a reachable state.*

1. *If  $s = (\lambda x. D(x), C, L, l \cdot T)$  then  $s$  is not final.*
2. *If  $s = (u, \underline{C} \langle D(x) [x \leftarrow \langle \cdot \rangle] \rangle, l \cdot L, T)$  then  $s$  is not final.*

**PROOF.** For point 1, by the exhaustible invariant (Prop. 7.4),  $s$  is exhaustible. Then, its tape test  $(\lambda x. D(x), C, L, l)$  does at least one transition towards a state  $s' \neq s$  surrounding  $l$ . Point 2 is analogous, just consider the log test  $s' = (u, \underline{C} \langle D(x) [x \leftarrow \langle \cdot \rangle] \rangle, l \cdot L, \epsilon)$ .  $\square$

The second consequence is that backtracking always succeeds.

**LEMMA 7.6 (BACKTRACKING ALWAYS SUCCEEDS).** *Let  $s$  a reachable state. If  $s \rightarrow_{\text{bt}1, l} s'$  then there is  $s''$  such that  $s' \xrightarrow{*}_{\lambda\text{IAM}} \rightarrow_{\text{bt}2, l} s''$ .*

PROOF. Consider  $s = (t, C\langle u \cdot \cdot \rangle, l \cdot L, T) \rightarrow_{\text{bt}1, l} (u, C\langle \cdot \rangle t), L, l \cdot T) = s'$ . Since  $s'$  is reachable then it is exhaustible, and so its tape test  $s'_l := (u, C\langle \cdot \rangle t), L, l$  can be exhausted, that is, there is a run  $\pi : s'_l \rightarrow_{\lambda\text{IAM}}^* q$  for a state  $q$  surrounding  $l$ . Note that  $s'_l$  is  $s'$  with empty tape. Now, we lift  $\pi$  to a run  $\pi^T : s' \rightarrow_{\lambda\text{IAM}}^* s''$  by using the tape lifting lemma (Lemma 5.6).  $\square$

Finally, we characterize final states, as anticipated in Section 5.

LEMMA 7.7 (FINAL STATES). *Let  $s_t \rightarrow_{\lambda\text{IAM}}^* s$  be a run ending on a final state. Then  $s$  has one of the following three shapes:  $(\lambda x.u, C, L, \epsilon)$ ,  $(y, C, L, \bullet^j)$  with  $y$  free in  $C(y)$ , or  $(t, \langle \cdot \rangle, \epsilon, \bullet^m \cdot l \cdot \bullet^j)$ .*

PROOF. The  $\lambda$ -IAM is never stuck on  $\rightarrow_{\bullet 1}$ ,  $\rightarrow_{\bullet 4}$ ,  $\rightarrow_{\text{es}}$ , and  $\rightarrow_{\text{es}2}$ . By the balance invariant (Lemma 5.2), it is also never stuck on  $\rightarrow_{\text{var}}$ ,  $\rightarrow_{\text{var}2}$ , and  $\rightarrow_{\text{bt}1}$  because the log has not enough entries. Note also that if the position is  $(t, C\langle \cdot \rangle u)$  and the direction is  $\uparrow$ , one among  $\rightarrow_{\bullet 3}$  and  $\rightarrow_{\text{arg}}$  always applies, as the tape cannot be empty, by the balance invariant. By Corollary 7.5, the  $\lambda$ -IAM cannot be stuck on  $\rightarrow_{\text{bt}2}$  and  $\rightarrow_{\text{var}3}$ . Then, the machine is stuck in three cases only. First, on  $\rightarrow_{\bullet 2}$  if the tape is empty—the stuck state is  $(\lambda x.u, C, L, \epsilon)$ . Second, in  $\rightarrow_{\text{var}}$  or  $\rightarrow_{\text{var}2}$  if the variable  $x$  is free—the state is  $(y, C, L, T)$ . Third, in direction  $\uparrow$  if the context is empty—the state is  $(t, \langle \cdot \rangle, \epsilon, T)$ .

Let's now characterize the shape of the tape  $T$ . For  $(y, C, L, T)$ , the tape  $T$  cannot contain any logged position. Otherwise, its tape would be  $T = \bullet^k \cdot l \cdot T'$  and by the exhaustible invariant, one could consider the tape test  $s_l = (y, C, L, \bullet^k \cdot l)$ , such that  $s_l \rightarrow_{\lambda\text{IAM}}^+ s'$ , where  $s'$  surrounds  $l$ . This is impossible since  $s_l$  is final as well. Finally, the  $\lambda$ -IAM stuck on  $\langle \cdot \rangle$  needs to have exactly one logged position on the tape. Indeed, the number  $n$  of logged positions on the tape has to be odd. However,  $n \leq 1$  because otherwise one could consider the tape test relative to the second logged position that would run in the same contradiction as in the previous case.  $\square$

*Log Tests and Position Changes.* To define the log test focussing on the  $m$ -th logged position  $l_m$  in the log of a state  $(t, C_n, l_n \cdot \cdot \cdot l_2 \cdot l_1, T, d)$ , we remove the prefix  $l_n \cdot \cdot \cdot l_{m+1}$  (if any), and move the current position up by  $n - m$  levels. Moreover, the tape is emptied and the direction is set to  $\uparrow$ . Let us define the position change.

Let  $(u, C_{n+1})$  be a position. Then, for every decomposition of  $n$  into two natural numbers  $m, k$  with  $m + k = n$ , we can find contexts  $C_m$  and  $C_k$ , and a term  $r$  satisfying exactly one of the two following conditions (levels can be incremented in two ways).

- Case  $t = C_m\langle rC_k\langle u \rangle \rangle$ . Then, the  $m + 1$ -outer context of the position  $(u, C_{n+1})$  is the context  $O_{m+1} := C_m\langle r\langle \cdot \rangle \rangle$  of level  $m + 1$  and the  $m + 1$ -outer position is  $(C_k\langle u \rangle, O_{m+1})$ .
- Case  $t = C_m\langle r[x \leftarrow C_k\langle u \rangle] \rangle$ . Then, the  $m + 1$ -outer context of the position  $(u, C_{n+1})$  is the context  $O_{m+1} := C_m\langle r[x \leftarrow \langle \cdot \rangle] \rangle$  of level  $m + 1$  and the  $m + 1$ -outer position is  $(C_k\langle u \rangle, O_{m+1})$ .

Note that the  $m$ -outer context and the  $m$ -outer position (of a given position) have level  $m$ . It is easy to realize that any position having level  $n$  has *unique*  $m$ -outer context and  $m$ -outer position, for every  $1 \leq m \leq n + 1$ , and that, moreover, outer positions are hereditary, in the following sense: the  $i$ -outer position of the  $m$ -outer position of  $(u, C_{n+1})$  is exactly the  $i$ -outer position of  $(u, C_{n+1})$ .

*Definition 7.8 (Log tests).* Let  $s = (t, C_n, l_n \cdot \cdot \cdot l_2 \cdot l_1, T, d)$  be a state with  $1 \leq m \leq n$ , and  $(u, O_m)$  be the  $m$ -outer position of  $(t, C_n)$ . The  $m$ -log test of  $s$  of focus  $l_m$  is the state  $s_{l_m} := (u, O_m, l_m \cdot \cdot \cdot l_2 \cdot l_1, \epsilon, \uparrow)$ .

## 8 IMPROVEMENTS, CONCRETELY

In this section we define an improvement  $\blacktriangleright$  relation for the  $\lambda$ -IAM, to be used in the sequel to prove soundness and adequacy.

Given a  $\rightarrow_{\text{lh}}$ -step  $t \rightarrow_{\text{lh}} u$ , the improvement  $\blacktriangleright$  has to relate states of code  $t$  with states of code  $u$ . Since  $\rightarrow_{\text{lh}}$  is the union of the three rewriting rules  $\rightarrow_{\text{dB}}$ ,  $\rightarrow_{\text{ls}}$  and  $\rightarrow_{\text{gc}}$ , we are going to define  $\blacktriangleright$  as the union of three improvements  $\blacktriangleright_{\text{dB}}$ ,  $\blacktriangleright_{\text{ls}}$ , and  $\blacktriangleright_{\text{gc}}$ .

*Improvement for  $\rightarrow_{\text{dB}}$ .* Lifting a step  $t \rightarrow_{\text{lh}} u$  to a relation between a  $\lambda$ -IAM state  $s$  of code  $t$  and a state  $q$  of code  $u$  requires changing all positions relative to  $t$  in  $s$  to positions relative to  $u$  in  $q$ . Note that all the positions in the token have to be changed, so that  $\blacktriangleright$  has to relate positions, logged positions, tape, log, and states.

*Explaining the Need of Context Rewriting.* A second more technical aspect is that one needs to extend linear head evaluation to contexts. Consider a step  $t \rightarrow_{\text{dB}} u$  where—for simplicity—the redex is at top level and the associated state  $(\langle \lambda x.r \rangle S w, \langle \cdot \rangle, \epsilon, \epsilon)$  has an empty token. This should be  $\blacktriangleright_{\text{dB}}$ -related to a state  $(\langle r[x \leftarrow w] \rangle S, \langle \cdot \rangle, \epsilon, \epsilon)$ . Let's have a look at how the two states evolve:

$$\begin{array}{ccc}
 \langle \langle \lambda x.r \rangle S w, \langle \cdot \rangle, \epsilon, \epsilon \rangle & \blacktriangleright_{\text{dB}} & \langle \langle r[x \leftarrow w] \rangle S, \langle \cdot \rangle, \epsilon, \epsilon \rangle \\
 \downarrow & & \downarrow_{|S|} \\
 \langle \langle \lambda x.r \rangle S, \langle \cdot \rangle w, \epsilon, \epsilon \rangle & & \langle r[x \leftarrow w], S, \epsilon, \epsilon \rangle \\
 \downarrow_{|S|} & & \downarrow \\
 \langle \lambda x.r, S w, \epsilon, \epsilon \rangle & & \langle r, \langle \langle \cdot \rangle [x \leftarrow w] \rangle S, \epsilon, \epsilon \rangle \\
 \downarrow & & \\
 \langle r, \langle \lambda x.\langle \cdot \rangle \rangle S w, \epsilon, \epsilon \rangle & & 
 \end{array}$$

To close the diagram, we need  $\blacktriangleright_{\text{dB}}$  to relate the two bottom states. Note that their relation can be seen as a  $\rightarrow_{\text{dB}}$  step involving the contexts of the two positions. Therefore we extend the definition of  $\rightarrow_{\text{dB}}$  to contexts adding the following top level clause (then included in  $\rightarrow_{\text{dB}}$  via a closure by head contexts):  $\langle \lambda x.C \rangle S t \mapsto_{\text{dB}} \langle C[x \leftarrow t] \rangle S$ . The new clause, in turn, requires a further extension of  $\rightarrow_{\text{dB}}$  (again closed by head contexts):  $\langle \lambda x.t \rangle S C \mapsto_{\text{dB}} \langle t[x \leftarrow C] \rangle S$ .

Note that in the shown local bisimulation diagram the right side is shorter. This is typical of when the machine travels through the redex. Outside of the redex, however, the two sides have the same length, as the next example shows—example that also motivates a further extension of  $\rightarrow_{\text{dB}}$  to contexts. Consider the case where  $t \rightarrow_{\text{dB}} u$  and the diagram is (the states do a  $\rightarrow_{\text{arg}}$  transition):

$$\begin{array}{ccc}
 \langle t, \langle \cdot \rangle r, \epsilon, l \rangle & \blacktriangleright_{\text{dB}} & \langle u, \langle \cdot \rangle r, \epsilon, l \rangle \\
 \downarrow & & \downarrow \\
 \langle r, t\langle \cdot \rangle, l, \epsilon \rangle & & \langle r, u\langle \cdot \rangle, l, \epsilon \rangle
 \end{array}$$

We then need to extend  $\rightarrow_{\text{dB}}$  so that  $t\langle \cdot \rangle \rightarrow_{\text{dB}} u\langle \cdot \rangle$ . A similar situation happens also when entering an ES with transition  $\rightarrow_{\text{var}2}$ . To close these diagrams, we add two further cases of reduction on contexts. Note that this time they have to be expressed via steps on terms (then included in  $\rightarrow_{\text{dB}}$  via a closure by head contexts), as their direct definition would require contexts with two holes. Of course, the same situation arises with  $\text{ls}$  and  $\text{gc}$  steps.

$$\frac{t \rightarrow_a u}{tC \rightarrow_a uC} \quad \frac{t \rightarrow_a u}{t[x \leftarrow C] \rightarrow_a u[x \leftarrow C]} \quad a \in \{\text{dB}, \text{ls}, \text{gc}\}.$$

*Definition 8.1.* The (overloaded) binary relation  $\triangleright_{\text{dB}}$  between positions, stacks, and states is defined by the following rules<sup>11</sup>.

$$\begin{array}{c} \frac{t \rightarrow_{\text{dB}} u}{(t, H) \triangleright_{\text{dB}}(u, H)} \text{rdx}_{\text{dB}} \\ \frac{}{\epsilon \triangleright_{\text{dB}} \epsilon} \text{tok1}_{\text{dB}} \\ \frac{(x, C) \triangleright_{\text{dB}}(x, D) \quad L \triangleright_{\text{dB}} L'}{(x, C, L) \triangleright_{\text{dB}}(x, D, L')} \text{pos}_{\text{dB}} \\ \frac{(t, C) \triangleright_{\text{dB}}(u, D) \quad T \triangleright_{\text{dB}} T' \quad L \triangleright_{\text{dB}} L' \quad d = d'}{(t, C, L, T, d) \triangleright_{\text{dB}}(u, D, L', T', d')} \text{state}_{\text{dB}} \end{array} \quad \begin{array}{c} \frac{C \rightarrow_{\text{dB}} D}{(t, C) \triangleright_{\text{dB}}(t, D)} \text{ctx}_{\text{dB}} \\ \frac{T \triangleright_{\text{dB}} T'}{\bullet \cdot T \triangleright_{\text{dB}} \bullet \cdot T'} \text{tok2}_{\text{dB}} \\ \frac{l \triangleright_{\text{dB}} l' \quad \Gamma \triangleright_{\text{dB}} \Gamma'}{l \cdot \Gamma \triangleright_{\text{dB}} l' \cdot \Gamma'} \text{tok3}_{\text{dB}} \end{array}$$

Note that  $\triangleright_{\text{dB}}$  contains all pairs  $((\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k), (\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^k))$ , where  $t \rightarrow_{\text{dB}} u$ , i.e. all the pairs of initial states involving a dB-redex.

*Improvement for  $\rightarrow_{\text{ls}}$ .* As for  $\rightarrow_{\text{dB}}$ , the improvement for  $\rightarrow_{\text{ls}}$  requires extending the rewriting relation to contexts. There are however some new subtleties. Given  $t \rightarrow_{\text{dB}} u$  and a position  $(r, C)$  for  $t$ , for  $\triangleright_{\text{dB}}$  the redex in  $t$  falls always entirely either in  $t$  or  $C$ . If  $t \rightarrow_{\text{ls}} u$ , instead, the redex can be split between the two. Consider the following diagram (where to simplify we assume the step to be at top level and the token to be empty).

$$\begin{array}{ccc} (H\langle x \rangle[x \leftarrow r], \langle \cdot \rangle, \epsilon, \epsilon) & \triangleright_{\text{ls}} & (H\langle r \rangle[x \leftarrow r], \langle \cdot \rangle, \epsilon, \epsilon) \\ \downarrow & & \downarrow \\ (H\langle x \rangle, \langle \cdot \rangle[x \leftarrow r], \epsilon, \epsilon) & & (H\langle r \rangle, \langle \cdot \rangle[x \leftarrow r], \epsilon, \epsilon) \end{array}$$

To close it, we have to  $\triangleright_{\text{ls}}$ -relate the two bottom states, where the pattern of the redex/reduct is split between the two parts of the position. This motivates clause rdx2 in the definition of  $\triangleright_{\text{ls}}$  below.

The new rule comes with consequences. Consider the following diagram involving the new clause for  $\triangleright_{\text{ls}}$ :

$$\begin{array}{ccc} (x, H[x \leftarrow t], \epsilon, \epsilon) & \triangleright_{\text{ls}} & (t, H[x \leftarrow t], \epsilon, \epsilon) =: q \\ \downarrow & & \\ s := (t, H\langle x \rangle[x \leftarrow \langle \cdot \rangle], \epsilon, \epsilon) & & \end{array}$$

To close the diagram, we have to  $\triangleright_{\text{ls}}$ -relate  $s$  and  $q$ . There are, however, two delicate points. First, we cannot see the context  $H\langle x \rangle[x \leftarrow \langle \cdot \rangle]$  as making a  $\rightarrow_{\text{ls}}$  step towards  $H[x \leftarrow t]$ , because  $t$  does not occur in  $H\langle x \rangle[x \leftarrow \langle \cdot \rangle]$ . For that, we have to introduce a variant of  $\rightarrow_{\text{ls}}$  on contexts that is parametric in  $t$  (and more general than the one to deal with the showed simplified diagram):

$$H\langle x \rangle[x \leftarrow C] \mapsto_{\text{ls}, t} H\langle C \rangle[x \leftarrow C\langle t \rangle].$$

The second delicate point of diagram (1) is that the extension of  $\triangleright_{\text{ls}}$  has to also  $\triangleright_{\text{ls}}$ -relate logs of different length, namely  $\epsilon$  and  $(x, H[x \leftarrow t], \epsilon)$ . This happens because positions of the two states do isolate the same term, but at different depths, as one is in the ES. Then the definition of  $\triangleright_{\text{ls}}$  has two clauses, one for logs ( $\text{pos2}_{\text{ls}}$ ) and one for states ( $\text{state2}_{\text{ls}}$ ), to handle such a case. The mismatch in log lengths is at most 1.

*Definition 8.2.* The binary relation  $\triangleright_{\text{ls}}$  is defined by:

$$\begin{array}{c} \frac{t \rightarrow_{\text{ls}} u}{(t, H) \triangleright_{\text{ls}}(u, H)} \text{rdx}_{\text{ls}} \\ \frac{K = K' \langle G[x \leftarrow t] \rangle}{(H\langle x \rangle, K) \triangleright_{\text{ls}}(H\langle t \rangle, K)} \text{rdx2} \\ \frac{T \triangleright_{\text{ls}} T'}{\bullet \cdot T \triangleright_{\text{ls}} \bullet \cdot T'} \text{tok2}_{\text{ls}} \\ \frac{(x, C) \triangleright_{\text{ls}}(x, D) \quad L \triangleright_{\text{ls}} L'}{(x, C, L) \triangleright_{\text{ls}}(x, D, L')} \text{pos}_{\text{ls}} \\ \frac{(t, C) \triangleright_{\text{ls}}(u, D) \quad T \triangleright_{\text{ls}} T' \quad L \triangleright_{\text{ls}} L' \quad d = d'}{(t, C, L, T, d) \triangleright_{\text{ls}}(u, D, L', T', d')} \text{state}_{\text{ls}} \\ \frac{C \rightarrow_{\text{ls}, t} D \quad T \triangleright_{\text{ls}} T' \quad L \triangleright_{\text{ls}} L' \quad d = d'}{(t, C, L \cdot l, T, d) \triangleright_{\text{ls}}(t, D, L', T', d')} \text{state2}_{\text{ls}} \end{array} \quad \begin{array}{c} \frac{C \rightarrow_{\text{ls}} D}{(t, C) \triangleright_{\text{ls}}(t, D)} \text{ctx}_{\text{ls}} \\ \frac{}{\epsilon \triangleright_{\text{ls}} \epsilon} \text{tok1}_{\text{ls}} \\ \frac{l \triangleright_{\text{ls}} l' \quad \Gamma \triangleright_{\text{ls}} \Gamma'}{l \cdot \Gamma \triangleright_{\text{ls}} l' \cdot \Gamma'} \text{tok3}_{\text{ls}} \\ \frac{C \rightarrow_{\text{ls}, x} D \quad L \triangleright_{\text{ls}} L'}{(x, C, L \cdot l) \triangleright_{\text{ls}}(x, D, L')} \text{pos2}_{\text{ls}} \\ \frac{C \rightarrow_{\text{ls}, x} D \quad L \triangleright_{\text{ls}} L'}{(x, C, L \cdot l) \triangleright_{\text{ls}}(x, D, L')} \text{pos2}_{\text{ls}} \end{array}$$

Note that  $\triangleright_{\text{ls}}$  contains all pairs  $((\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k), (\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^k))$ , where  $t \rightarrow_{\text{ls}} u$ , i.e. all the initial states containing a ls-redex and its reduct.

*Improvement for  $\rightarrow_{\text{gc}}$ .* The candidate improvement  $\triangleright_{\text{gc}}$  induced by  $\rightarrow_{\text{gc}}$  requires an extension of  $\rightarrow_{\text{gc}}$  with a rule on contexts similar to the parametric one for  $\rightarrow_{\text{ls}}$ . Let  $t[x \leftarrow u] \rightarrow_{\text{gc}} t$  and consider:

$$\begin{array}{ccc} (t[x \leftarrow u], \langle \cdot \rangle, \epsilon, \epsilon) & \triangleright_{\text{gc}} & (t, \langle \cdot \rangle, \epsilon, \epsilon) \\ \downarrow & & \\ (t, \langle \cdot \rangle[x \leftarrow u], \epsilon, \epsilon) & & \end{array}$$

To close the diagram, we extend the definition of  $\rightarrow_{\text{gc}}$  to context with the following parametric rule (closed by head contexts):

$$C[x \leftarrow u] \mapsto_{\text{gc}, t} C \quad \text{if } x \notin \text{fv}(t) \cup \text{fv}(C).$$

*Definition 8.3.* The binary relation  $\triangleright_{\text{gc}}$  is defined as follows.

$$\begin{array}{c} \frac{t \rightarrow_{\text{gc}} u}{(t, H) \triangleright_{\text{gc}}(u, H)} \text{rdx}_{\text{gc}} \\ \frac{}{\epsilon \triangleright_{\text{gc}} \epsilon} \text{tok1}_{\text{gc}} \\ \frac{T \triangleright_{\text{gc}} T'}{\bullet \cdot T \triangleright_{\text{gc}} \bullet \cdot T'} \text{tok2}_{\text{gc}} \\ \frac{(x, C) \triangleright_{\text{gc}}(x, D) \quad L \triangleright_{\text{gc}} L'}{(x, C, L) \triangleright_{\text{gc}}(x, D, L')} \text{pos}_{\text{gc}} \\ \frac{(t, C) \triangleright_{\text{gc}}(u, D) \quad T \triangleright_{\text{gc}} T' \quad L \triangleright_{\text{gc}} L' \quad d = d'}{(t, C, L, T, d) \triangleright_{\text{gc}}(u, D, L', T', d')} \text{state}_{\text{gc}} \end{array} \quad \begin{array}{c} \frac{C \rightarrow_{\text{gc}} D}{(t, C) \triangleright_{\text{gc}}(t, D)} \text{ctx}_{\text{gc}} \\ \frac{C \rightarrow_{\text{gc}, t} D}{(t, C) \triangleright_{\text{gc}}(t, D)} \text{ctx2}_{\text{gc}} \\ \frac{l \triangleright_{\text{gc}} l' \quad \Gamma \triangleright_{\text{gc}} \Gamma'}{l \cdot \Gamma \triangleright_{\text{gc}} l' \cdot \Gamma'} \text{tok3}_{\text{gc}} \end{array}$$

The proof of the next theorem is a tedious easy check of diagrams.

**THEOREM 8.4.**  $\triangleright_{\text{ls}}, \triangleright_{\text{dB}}$  and  $\triangleright_{\text{gc}}$  are improvements.

## 9 SOUNDNESS AND ADEQUACY, PROVED

Here we use the improvements of the previous sections to prove soundness and adequacy. Consider  $\triangleright = \triangleright_{\text{dB}} \cup \triangleright_{\text{ls}} \cup \triangleright_{\text{gc}}$ , that is an improvement because its components are. Consequently, if  $t \rightarrow_{\text{lh}} u$ , then the  $\lambda$ -IAM run on  $u$  improves the one on  $t$ , that is,  $s_{t, k} = (\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k) \triangleright (\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^k) = s_{u, k}$ .

Improvements transfer more than termination/divergence along  $\rightarrow_{\text{lh}}$ . They also give bisimilar, structurally equivalent tapes, proving the invariance of the semantics, that is, soundness.

**THEOREM 9.1 (SOUNDNESS).** *If  $t \rightarrow_{\text{lh}} u$ , then  $\llbracket t \rrbracket_k = \llbracket u \rrbracket_k$  for each  $k \geq 0$ .*

<sup>11</sup> $\Gamma$  is a meta-variable that stands either for a log  $L$  or for a tape  $T$ .

PROOF. Since  $t \rightarrow_{\text{lh}} u$ , then  $s = (\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k) \blacktriangleright (\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^k) = q$  by the results about improvements (Theorem 8.4). Since improvements transfer termination/divergence (Prop. 1), we have  $\llbracket t \rrbracket_k = \perp$  iff  $\llbracket u \rrbracket_k = \perp$ . If  $\llbracket t \rrbracket_k \neq \perp$  let  $s'$  be the final state of  $s$ . Since  $\blacktriangleright$  is an improvement, there is a final state  $q' = (r', C', L', T', d)$  such that  $q \rightarrow_{\lambda\text{IAM}}^* q'$  and  $s' \blacktriangleright q'$ . Cases of  $s'$ :

- $s' = (\underline{\lambda x.w}, C, L, \epsilon)$ . Since  $s' \blacktriangleright q'$ , either  $\lambda x.w \rightarrow_{\text{lh}} r'$ , and thus  $r' = \lambda x.w'$  or  $C \rightarrow_{\text{lh}} C'$  and thus  $r' = \lambda x.w$ . Then, since  $\epsilon \blacktriangleright T'$ , also  $T' = \epsilon$  and thus  $\llbracket t \rrbracket_k = \llbracket u \rrbracket_k = \perp$ .
- $s' = (t, \langle \cdot \rangle, \epsilon, \bullet^m \cdot l \cdot \bullet^n)$ . Then, since  $s' \blacktriangleright q'$ ,  $C' = \langle \cdot \rangle$ , because the hole cannot  $\rightarrow_{\text{lh}}$ -reduce. Moreover, the structure of the tape is preserved by  $\blacktriangleright$ , in particular by its definition also  $T' = \bullet^m \cdot l' \cdot \bullet^n$  and thus  $\llbracket t \rrbracket_k = \llbracket u \rrbracket_k = \langle m, n \rangle$ .
- $s' = (\underline{x}, C, L, \bullet^m)$ . Since a variable cannot  $\rightarrow_{\text{lh}}$ -reduce, also  $r' = x$ . Then, since the structure of the tape is preserved by  $\blacktriangleright$ , also  $T' = \bullet^m$  and thus  $\llbracket t \rrbracket_k = \llbracket u \rrbracket_k = \langle x, m \rangle$ .  $\square$

*Adequacy.* Adequacy is the fact that  $\llbracket t \rrbracket$  is successful if and only if  $\rightarrow_{\text{lh}}$  terminates. We prove the two directions separately.

*Direction  $\lambda\text{-IAM}$  to  $\rightarrow_{\text{lh}}$ .* The *only if* direction of the statement is easy to prove. Since  $\llbracket t \rrbracket_k$  is invariant by  $\rightarrow_{\text{lh}}$  (soundness) and  $\rightarrow_{\text{lh}}$  terminates on  $t$ , we can as well assume that  $t$  is normal. The rest is given by the following proposition.

PROPOSITION 9.2 (READING THE HEAD VARIABLE ON  $\rightarrow_{\text{lh}}$ -NORMAL FORMS). *Let  $t = \lambda x_1 \dots \lambda x_n . y u_1 \dots u_l$  be a head (linear) normal form, where  $n, l \geq 0$ . If  $y = x_m$  where  $1 \leq m \leq n$ , then  $\llbracket t \rrbracket_{n+k} = \langle m-1, l+k \rangle$ , otherwise, if  $y$  is free, then  $\llbracket t \rrbracket_{n+k} = \langle y, l+k \rangle$ . Moreover, if  $n \geq 1$  and  $0 \leq k \leq n-1$ , then  $\llbracket t \rrbracket_k = \perp$ .*

PROOF. We proceed computing  $\llbracket t \rrbracket_n$  explicitly. We have:

$$\begin{aligned} (\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^n) &\rightarrow_{\bullet_2}^n (\underline{y u_1 \dots u_l}, \lambda x_1 \dots \lambda x_n . \langle \cdot \rangle, \epsilon, \epsilon) \\ &\rightarrow_{\bullet_1}^l (\underline{y}, \lambda x_1 \dots \lambda x_n . \langle \cdot \rangle u_1 \dots u_l, \epsilon, \bullet^l) \end{aligned}$$

If  $y$  is free, the  $\lambda\text{-IAM}$  stops and  $\llbracket t \rrbracket_n = \langle y, l \rangle$ . By lifting (Lemma 5.6), one has that  $\llbracket t \rrbracket_{n+k} = \langle y, l+k \rangle$ . Otherwise, if  $y$  is bound by a  $\lambda$ -abstraction, i.e.  $y = x_m$  for  $1 \leq m \leq n$ , the computation continues.

$$\begin{aligned} (\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^n) &\rightarrow_{\lambda\text{IAM}}^{n+l} \\ (\underline{x_m}, \lambda x_1 \dots \lambda x_n . \langle \cdot \rangle u_1 \dots u_l, \epsilon, \bullet^l) &\rightarrow_{\text{var}} \\ (\lambda x_m \dots \lambda x_n . x_m u_1 \dots u_l, \lambda x_1 \dots \lambda x_{m-1} . \langle \cdot \rangle, l \cdot \bullet^l, \epsilon) &\rightarrow_{\bullet_4}^{m-1} \\ (u, \langle \cdot \rangle, \epsilon, \bullet^{m-1} \cdot l \cdot \bullet^l) & \end{aligned}$$

where  $l = (x_m, \lambda x_1 \dots \lambda x_n . \langle \cdot \rangle u_1 \dots u_l, \epsilon)$ . Then  $\llbracket t \rrbracket_n = \langle m-1, l \rangle$ . By lifting (Lemma 5.6), one has that  $\llbracket t \rrbracket_{n+k} = \langle m-1, l+k \rangle$ .

Moreover, please note that if  $n \geq 1$  and  $0 \leq k \leq n-1$ , we have:

$$(\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k) \rightarrow_{\bullet_2}^k (\underline{\lambda x_k \dots \lambda x_n . y u_1 \dots u_l}, \lambda x_1 \dots \lambda x_{k-1} . \langle \cdot \rangle, \epsilon, \epsilon)$$

and thus  $\llbracket t \rrbracket_k = \perp$ .  $\square$

*Direction  $\rightarrow_{\text{lh}}$  to  $\lambda\text{-IAM}$ .* The proof of the *if* direction of the adequacy theorem is by contra-position: if the  $\rightarrow_{\text{lh}}$  diverges on  $t$  then no run of the  $\lambda\text{-IAM}$  on  $t$  ends in a successful state.

The proof is obtained via a quantitative analysis of the improvements, showing that the length of runs *strictly* decreases along  $\rightarrow_{\text{lh}}$ . Note indeed that improvements guarantee only that the length of runs does not increase. To prove that it actually decreases one needs an additional *global* analysis of runs—improvements only deal with *local* bisimulation diagrams. On proof nets, this decreasing property

correspond to the standard fact that IAM paths passing through a cut have shorter residuals after that cut.

We recall that we write  $|t|_k$  for the length of the run  $(\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k)$ , with the convention that  $|t|_k = \infty$  if the machine diverges.

LEMMA 9.3 (THE LENGTH OF TERMINATING RUNS STRICTLY DECREASES ALONG  $\rightarrow_{\text{lh}}$ ). *Let  $t \rightarrow_{\text{lh}} u$  and  $|t|_k \neq \infty$ . There exists  $k \geq 0$  such that  $|t|_h > |u|_h$  for each  $h \geq k$ .*

PROOF. We treat the case of  $t \rightarrow_{\text{dB}} u$ , the others are obtained via similar diagrams. If  $t$  has a  $\rightarrow_{\text{dB}}$ -redex then it has the shape  $t = H \langle \langle \lambda x.r \rangle S w \rangle$  and  $u$  is in the form  $u = H \langle \langle r[x \leftarrow w] \rangle S \rangle$ . By induction on the structure of  $H$  one can prove that there exist  $k, n \geq 0$  such that  $(\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^k) \rightarrow_{\lambda\text{IAM}}^n (\langle \lambda x.r \rangle S w, H, \epsilon, \epsilon)$  and  $(\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^k) \rightarrow_{\lambda\text{IAM}}^n (\langle r[x \leftarrow w] \rangle S, H, \epsilon, \epsilon)$ . Given such  $n$  and  $k$  by the lifting lemma (Lemma 5.6) also the following holds: for any  $j \geq 0$ ,  $(\underline{t}, \langle \cdot \rangle, \epsilon, \bullet^j \cdot \bullet^k) \rightarrow_{\lambda\text{IAM}}^n (\langle \lambda x.r \rangle S w, H, \epsilon, \bullet^j)$  and  $(\underline{u}, \langle \cdot \rangle, \epsilon, \bullet^j \cdot \bullet^k) \rightarrow_{\lambda\text{IAM}}^n (\langle r[x \leftarrow w] \rangle S, H, \epsilon, \bullet^j)$ . Moreover, by definition of the improvement  $\blacktriangleright_{\text{dB}}$  we have the following diagram.

$$\begin{array}{ccc} (H \langle \langle \lambda x.r \rangle S w \rangle, \langle \cdot \rangle, \epsilon, \bullet^j \cdot \bullet^k) & \blacktriangleright_{\text{dB}} & (H \langle \langle r[x \leftarrow w] \rangle S \rangle, \langle \cdot \rangle, \epsilon, \bullet^j \cdot \bullet^k) \\ \downarrow^n & & \downarrow^n \\ (\langle \lambda x.r \rangle S w, H, \epsilon, \bullet^j) & \blacktriangleright_{\text{dB}} & (\langle r[x \leftarrow w] \rangle S, H, \epsilon, \bullet^j) \\ \downarrow & & \downarrow |S| \\ (\langle \lambda x.r \rangle S, H \langle \langle \cdot \rangle w \rangle, \epsilon, \bullet \cdot \bullet^j) & & (r[x \leftarrow w], H \langle \langle \cdot \rangle L \rangle, \epsilon, \bullet^j) \\ \downarrow |S| & & \downarrow \\ (\lambda x.r, H \langle S w \rangle, \epsilon, \bullet \cdot \bullet^j) & & (r, H \langle \langle \cdot \rangle [x \leftarrow w] \rangle S, \epsilon, \bullet^j) = s_2 \\ \downarrow & & \downarrow \\ s_1 = (\underline{r}, H \langle \langle \lambda x.\langle \cdot \rangle \rangle S w \rangle, \epsilon, \bullet^j) & \blacktriangleright_{\text{dB}} & (\underline{r}, H \langle \langle \cdot \rangle [x \leftarrow w] \rangle S, \epsilon, \bullet^j) = s_2 \end{array}$$

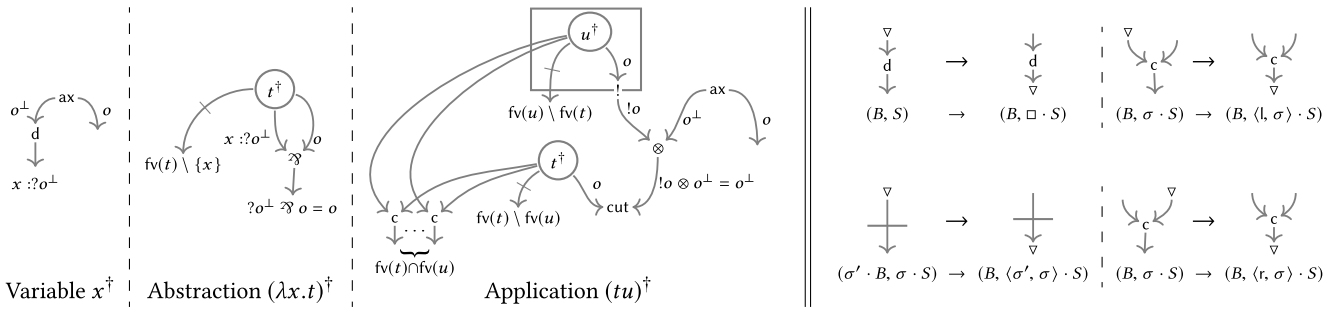
From  $s_1 \blacktriangleright_{\text{dB}} s_2$ , the hypothesis  $|t|_k \neq \infty$ , and the properties of improvements (Lemma 2), we obtain  $|s_1| \geq |s_2|$ . Then, by setting  $h := k+j$ , we have  $|t|_h = n+1+|S|+1+|s_1| > n+|S|+1+|s_2| = |u|_h$ .  $\square$

Using the lemma, we prove the *if* direction of adequacy, that then follows.

PROPOSITION 9.4 ( $\rightarrow_{\text{lh}}$ -DIVERGENCE IMPLIES THAT THE  $\lambda\text{-IAM}$  NEVER SUCCEEDS). *Let  $t$  be a  $\rightarrow_{\text{lh}}$ -divergent LSC term. There is no  $k \geq 0$  such that  $\llbracket t \rrbracket_k$  is successful.*

PROOF. By contradiction, suppose that there exists  $k$  such that  $\llbracket t \rrbracket_k$  is successful. Then by soundness  $|t|_k = n \in \mathbb{N}$  and it ends on a successful state. By monotonicity of runs (Lemma 5.7),  $|t|_k = |t|_h = n$  for every  $h > k$ . Since  $t \rightarrow_{\text{lh}}$ -divergent, then there exists an infinite reduction sequence  $\rho : t = t_0 \rightarrow_{\text{lh}} t_1 \rightarrow_{\text{lh}} t_2 \rightarrow_{\text{lh}} \dots \rightarrow_{\text{lh}} t_k \rightarrow_{\text{lh}} \dots$ . Since the length of terminating runs strictly decreases along  $\rightarrow_{\text{lh}}$  for sufficiently long inputs (Lemma 9.3), for each  $i \in \mathbb{N}$  if  $t_i \rightarrow_{\text{lh}} t_{i+1}$  then there exists  $k_i$  such that  $|t_i|_{k_i} > |t_{i+1}|_{k_i}$ . Now, consider  $h = \max\{k_0, \dots, k_n, k_{n+1}\}$ . We have that  $|t_j|_h > |t_{j+1}|_h$  for every  $j \in \{0, 1, \dots, n+1\}$ . Then  $|t_0|_h \geq |t_{n+1}|_h + n + 1$ . Since the length of runs is non-negative, we obtain that  $|t_0|_h \geq n + 1$ , which is absurd because  $h \geq k_0$  and so  $|t_0|_h = n$ .  $\square$

THEOREM 9.5 (ADEQUACY). *Let  $t$  be a LSC term. Then  $t$  has  $\rightarrow_{\text{lh}}$ -normal form if and only if there exists  $h \geq 0$  such that for all  $k \geq h$   $\llbracket t \rrbracket_k$  is successful. Moreover,  $t$  has weak  $\rightarrow_{\text{lh}}$ -normal form if and only if either  $\llbracket t \rrbracket_0 = \perp$  or  $\llbracket t \rrbracket_0 = \langle x, n \rangle$  for some  $x \in \mathcal{V}$ ,  $n \geq 0$ .*



**Figure 6:** On the left, the call-by-name translation  $(\cdot)^\dagger$  of the  $\lambda$ -calculus into linear logic proof nets. On the right, transition rules of the proof nets presentation of the IAM related to exponential signatures.

## 10 COMPARISON WITH PROOF NETS

Here we sketch how the  $\lambda$ -IAM relates to the original presentation based on linear logic proof nets, due to Mackie and Danos & Regnier [25, 27, 41], the IAM. For lack of space, we avoid defining proof nets and related concepts, and focus only on the key points.

The  $\lambda$ -IAM corresponds to the IAM on proof nets representing  $\lambda$ -terms according to the call-by-name translation  $(\cdot)^\dagger$  in Fig. 6<sup>12</sup>, and considering only paths from the distinguished conclusion of the net, as in [25] (while [27, 41] use the call-by-value translation, and [27] considers paths starting on whatever conclusions).

There is a bisimulation between the  $\lambda$ -IAM and such a restricted IAM, which is not strong because two  $\lambda$ -IAM transitions rather are *macros*, packing together whole sequences of transitions in their presentation. Namely, transition  $\rightarrow_{\text{var}}$  short-circuits the path between a variable  $x$  and its abstraction  $\lambda x.C_n(x)$ . In proof nets, this path traverses a dereliction, exactly  $n$  auxiliary doors, possibly a contraction tree, and ends on the  $\wp$  representing the abstraction. The dual transition  $\rightarrow_{\text{bt2}}$  does the reverse job, corresponding to the reversed path. Aside the different notations and the *macrifaction*, our transitions correspond exactly to the actions attached to proof net edges presented in [27]<sup>13</sup>, as we explain next.

In the proof nets presentation the token is given by two stacks, called *boxes stack*  $B$  and *balancing stack*  $S$ , corresponding exactly to our log  $L$  and tape  $T$ , respectively. They are formed by sequences of multiplicative constants  $p$  (corresponding to our  $\bullet$ ) and by *exponential signatures*  $\sigma$ . They are defined by the following grammar<sup>14</sup>.

BALANCING STACKS	$S$	$::= \epsilon \mid p \cdot S \mid \sigma \cdot S$
BOXES STACKS	$B$	$::= \epsilon \mid \sigma \cdot B$
EXP. SIGNATURES	$\sigma, \sigma'$	$::= \square \mid \langle \sigma, \sigma' \rangle \mid \langle l, \sigma \rangle \mid \langle r, \sigma \rangle$

Intuitively, exponential signatures are binary trees with  $\square$ ,  $l$  or  $r$  as leaves, where  $l$  and  $r$  denote the left/right premise of a contraction. Fig. 6 shows the IAM transitions concerning exponential signatures that are the relevant difference with respect to the  $\lambda$ -IAM.

<sup>12</sup>The translation uses a recursive type  $o = ?o^\perp \wp o$  in order to be able to represent untyped terms of the  $\lambda$ -calculus—this is standard. Every net has a unique conclusion labeled with  $o$ , which is the *output*, and all the other conclusions have type  $?o^\perp$  and are labeled with a free variable of the term. In the abstraction case  $\lambda x.t$ , if  $x \notin fv(t)$  then a weakening is added to represent that variable.

<sup>13</sup>We refer to [27] rather than [25] because in [25] the definition is only sketched, while [27] is more accurate.

<sup>14</sup>With respect to [27]: for clarity, we use symbols  $l$  and  $r$  instead of  $p'$  and  $q'$ , and we omit  $q$ , dual of  $p$ , as in the call-by-name translation it is always and only next to exponential signatures, which then subsume it.

To explain how  $\rightarrow_{\text{var}}$  is simulated by the IAM, let's recall it:

$$(x, C\langle \lambda x.D_n \rangle, L_n \cdot L, T) \rightarrow_{\text{var}} (\lambda x.D_n(x), \square, L, (x, \lambda x.D_n, L_n) \cdot T).$$

The IAM does the same, just in more steps and with another syntax. Consider a token  $(B_n \cdot B, S)$  approaching a variable  $x$  that is  $n$  boxes deeper than its binder  $\lambda x.D_n(x)$ . Variables are translated as dereliction links and thus we have:  $(B_n \cdot B, S) \rightarrow (B_n \cdot B, \square \cdot S)$ .

Then, the token travels until the binder of  $x$  is found (a  $\wp$  in the proof net translation of the term), *i.e.* it traverses exactly  $n$  boxes always exiting from the auxiliary doors. Moreover, for every such box a contraction could be encountered. Let's first suppose that  $x$  is used linearly, so that no contractions are encountered. Then the token rewrites in the following way, traversing  $n$  auxiliary doors.

$$\begin{aligned} (\sigma_1 \cdot B_{n-1} \cdot B, \square \cdot S) &\rightarrow (\sigma_2 \cdot B_{n-2} \cdot B, \langle \sigma_1, \square \rangle \cdot S) \\ &\rightarrow (\sigma_3 \cdot B_{n-3} \cdot B, \langle \sigma_2, \langle \sigma_1, \square \rangle \rangle \cdot S) \\ &\rightarrow \dots \rightarrow (B, \langle \sigma_n, \langle \dots \langle \sigma_1, \square \rangle \dots \rangle \rangle \cdot S). \end{aligned}$$

Note the perfect matching between the two formulations: in both cases the first  $n$  logged positions/signatures in the log/boxes stack are removed from it and, once wrapped in a single logged position/signature, then put on the tape/balancing stack. In presence of contractions the exponential signature  $\langle \sigma_n, \langle \dots \langle \sigma_1, \square \rangle \dots \rangle \rangle$  is interleaved by  $l$  and  $r$  leaves. These symbols represent nothing more than a binary code used to traverse the contraction tree of  $x$ . In the  $\lambda$ -IAM, the same information is represented, more compactly, by specifying the variable occurrence via its position inside its binder.

## 11 CONCLUSIONS

This paper presents a direct proof of the implementation theorem for Mackie and Danos & Regnier's Interaction Abstract Machine, building over a natural notion of bisimulation and avoiding detours via game semantics. Additionally, it (re)formulates the machine directly on  $\lambda$ -terms, making it conceptually closer to traditional abstract machines, and more apt to formalizations in proof assistants.

Our work opens the way to a fine analysis of the complexity of the implementation of the  $\lambda$ -calculus, in particular regarding the space-time trade-off, by comparing the  $\lambda$ -IAM, that the literature suggests being tuned for space-efficiency, to traditional environment machines that are instead tuned for time-efficiency.

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